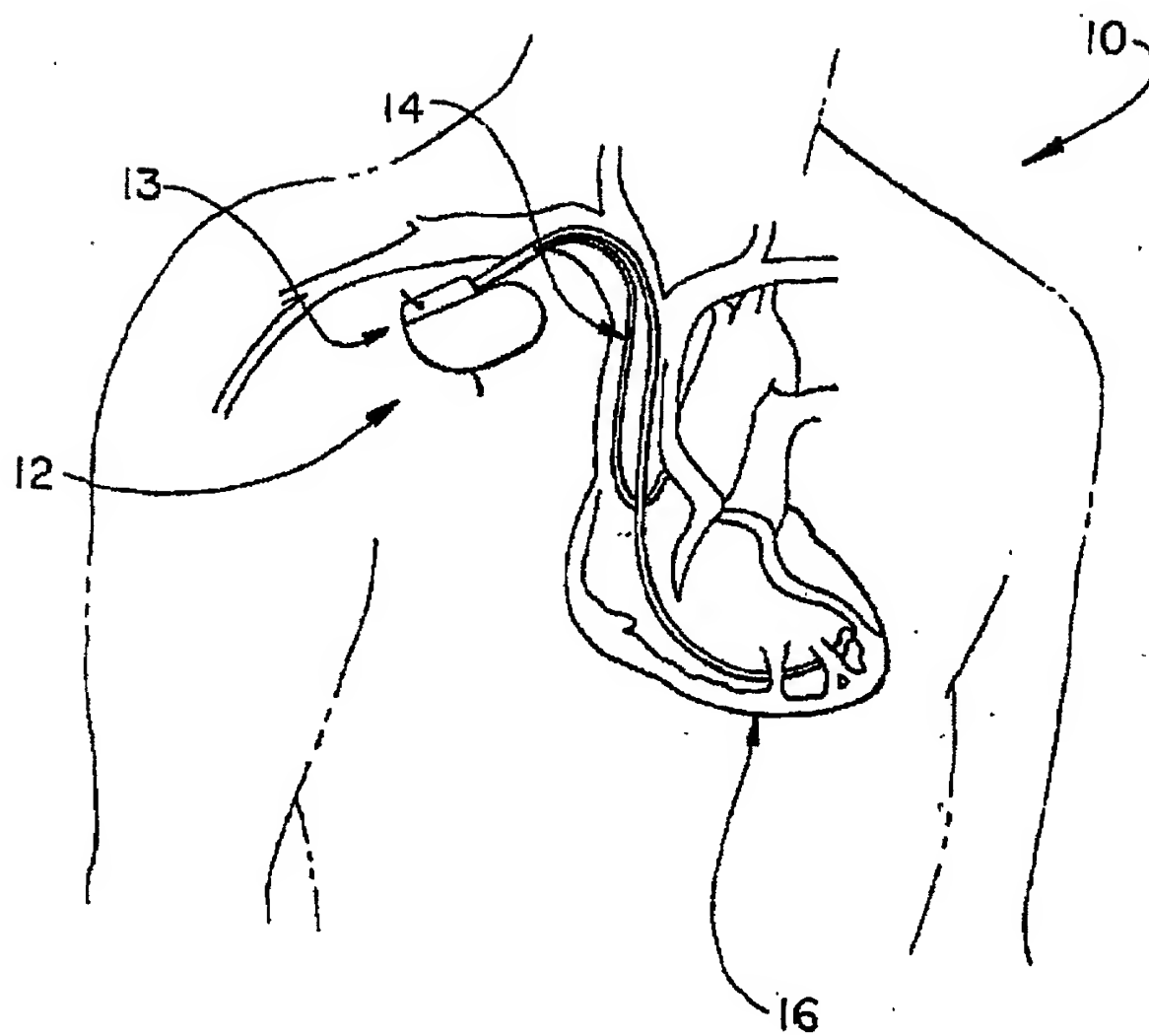


Fig. 1



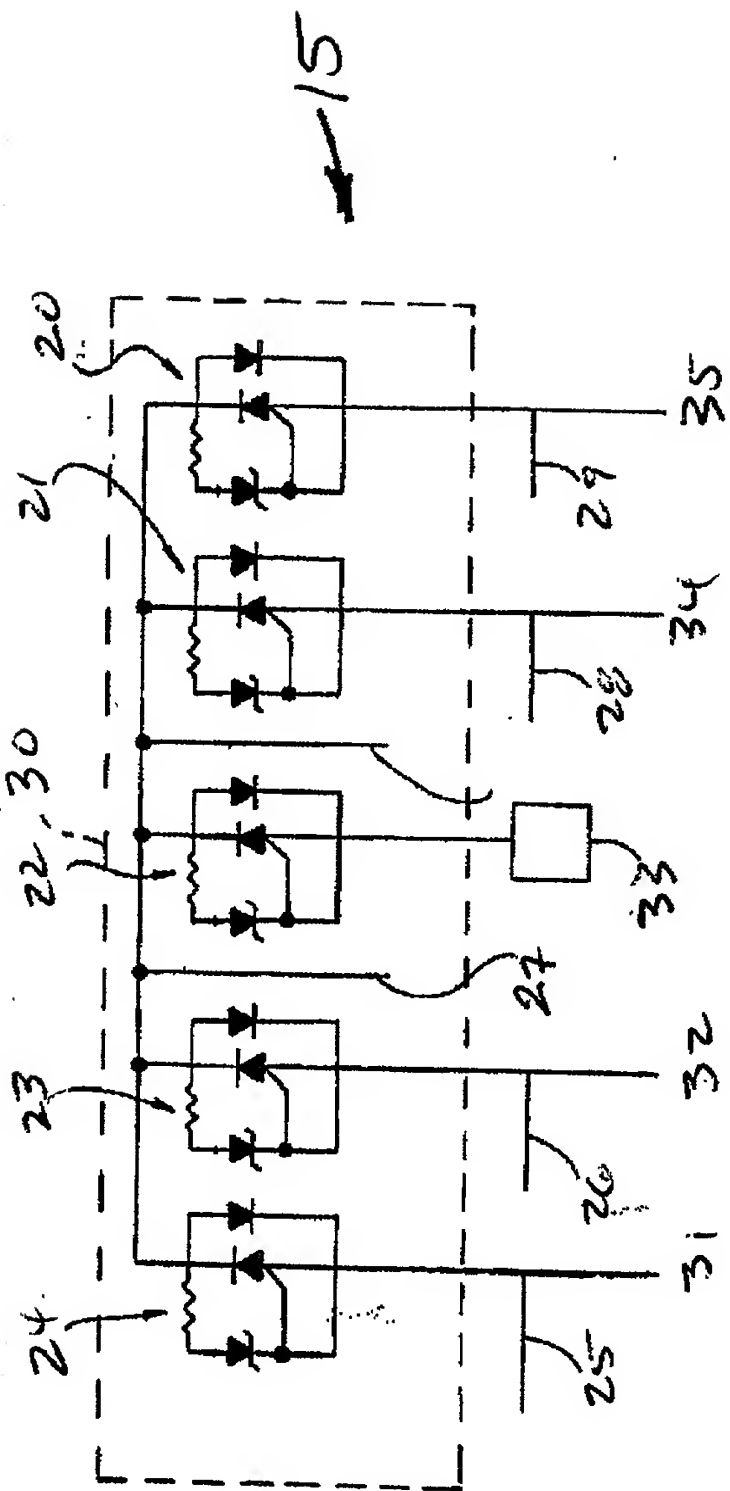


Fig. 2

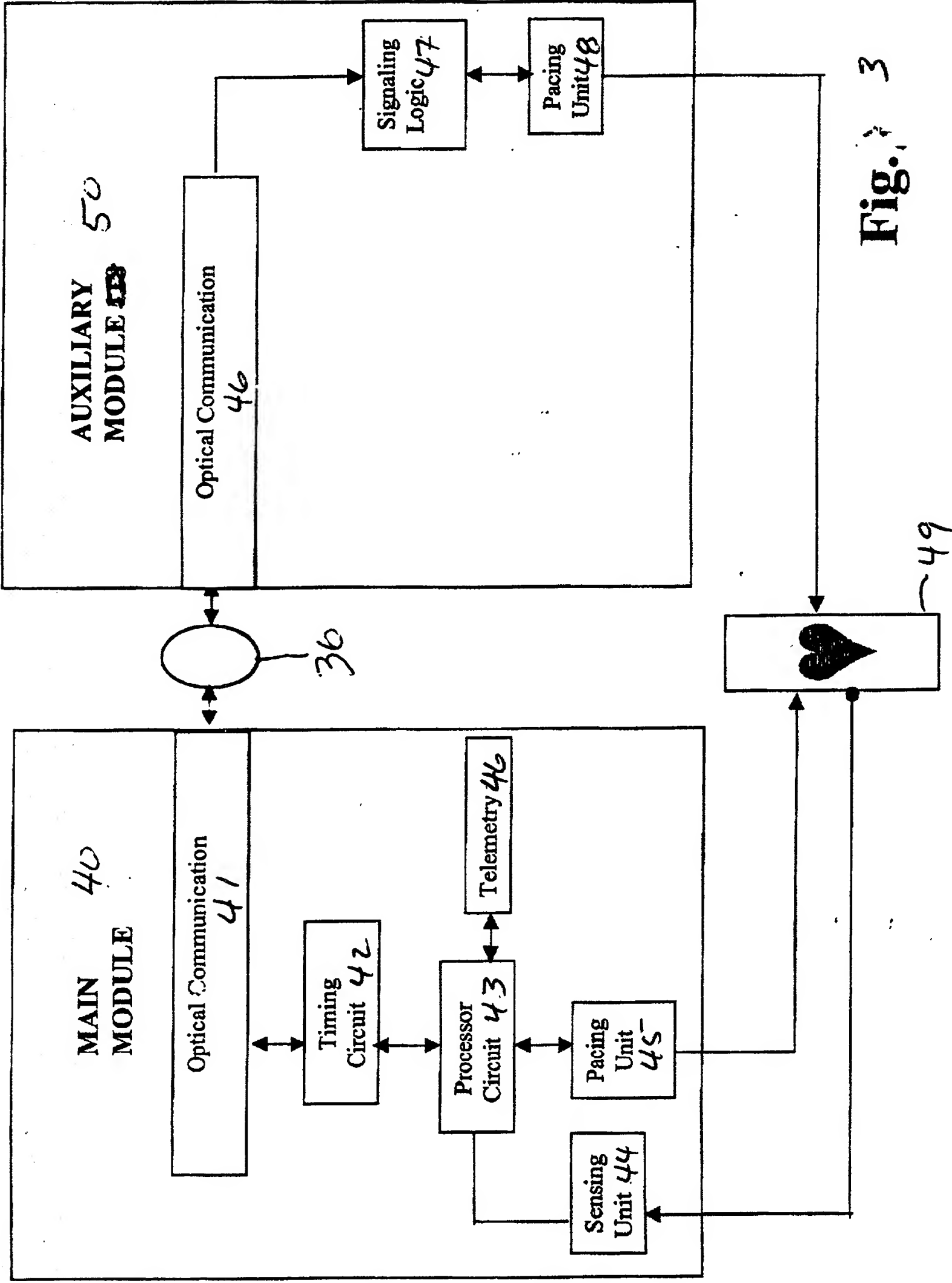


Fig. 3

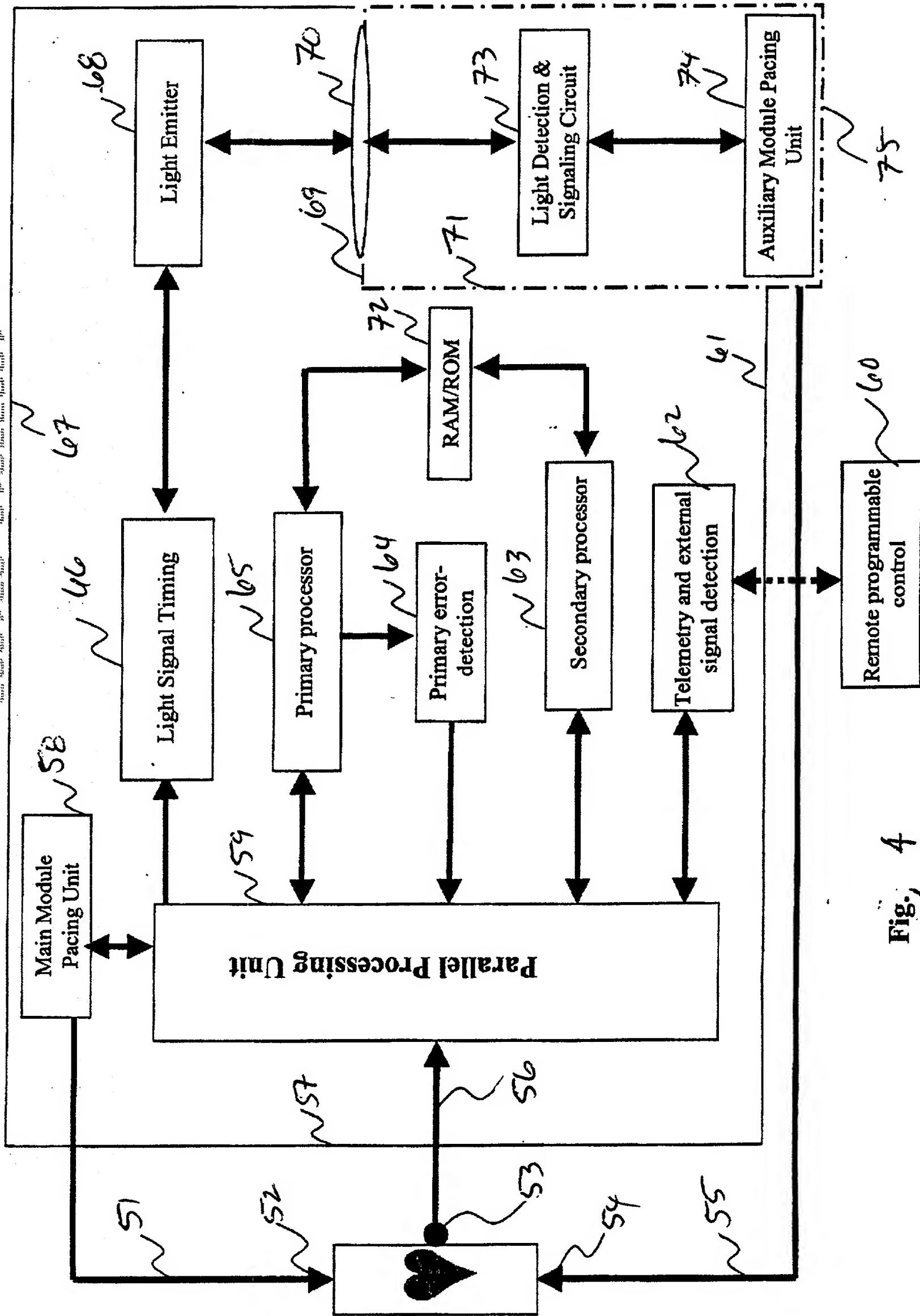


Fig. 4

570

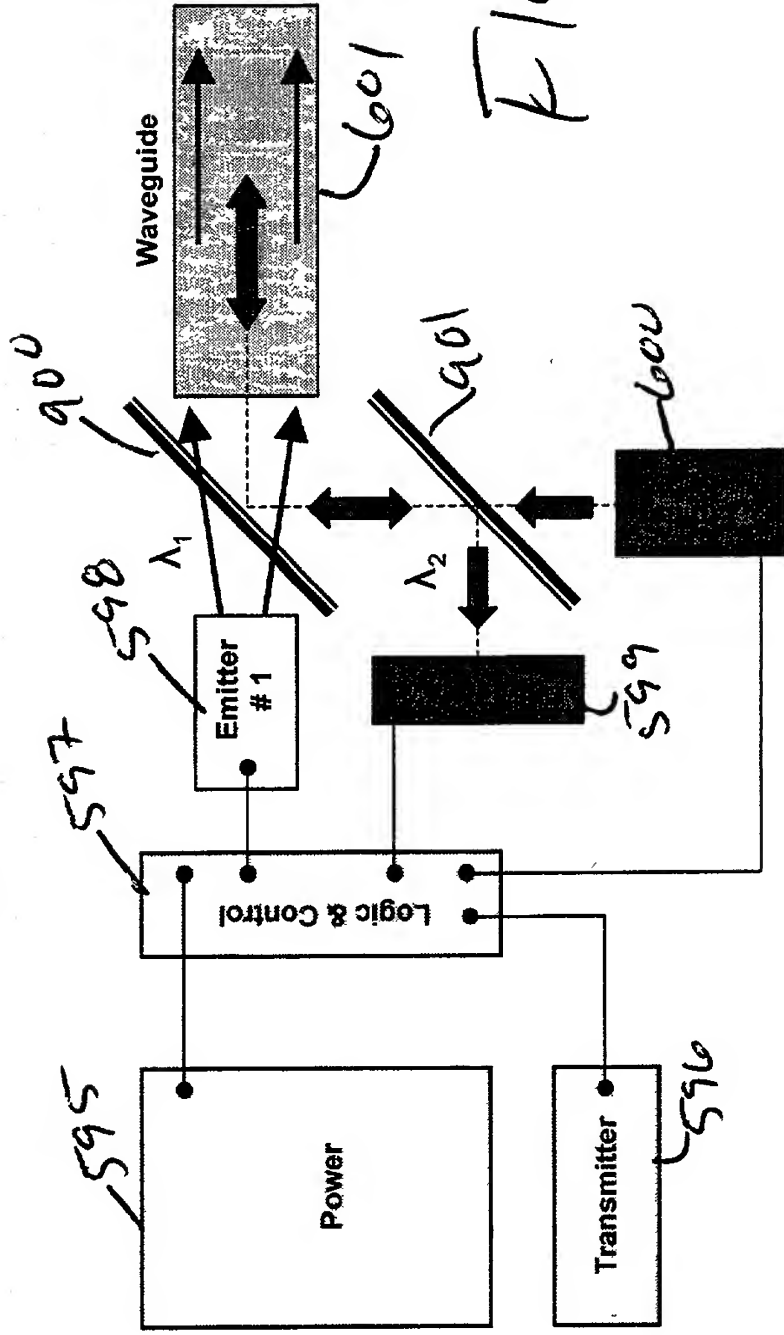


FIG. 5

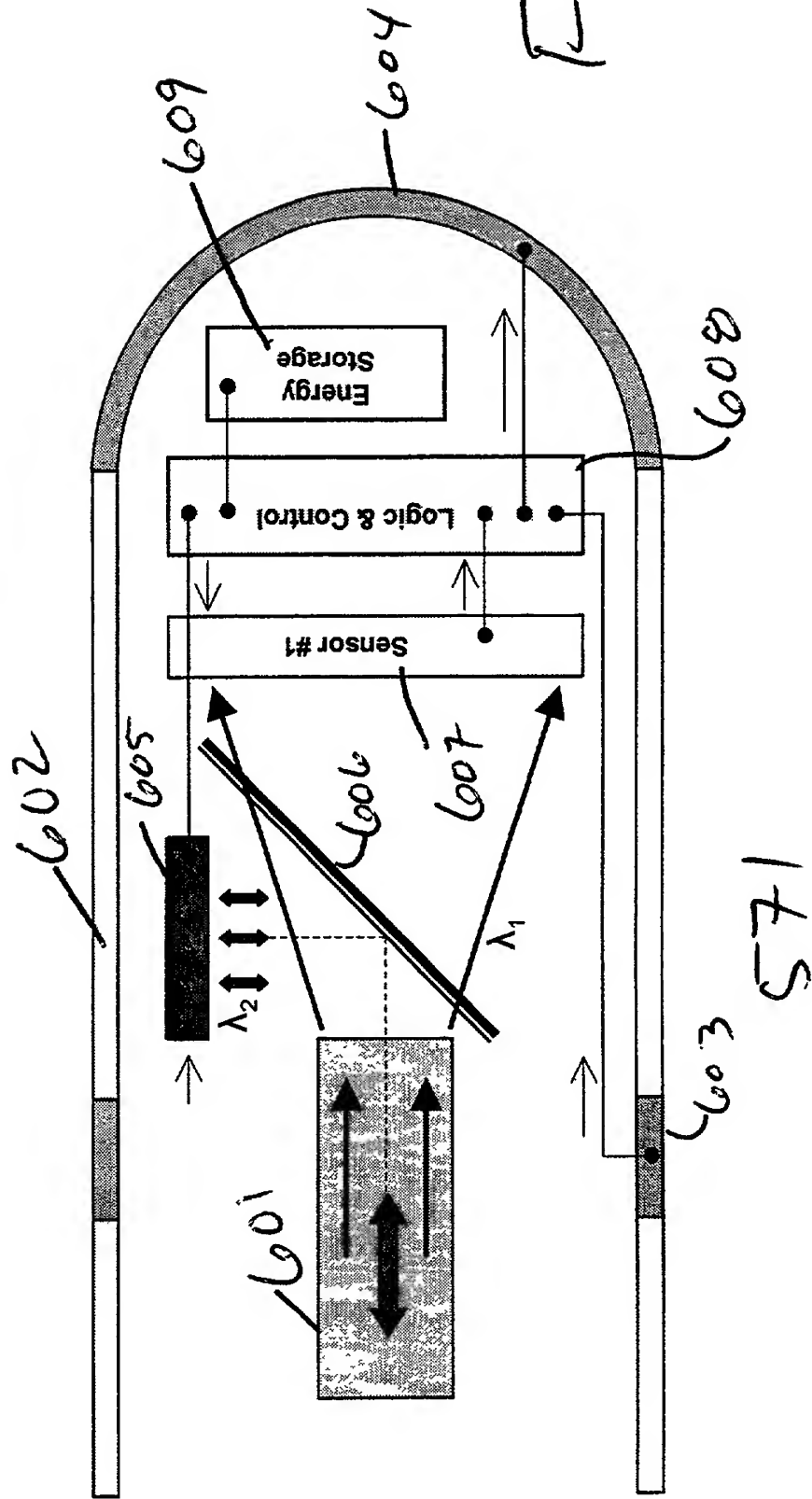


FIG. 6

571

FIG 7

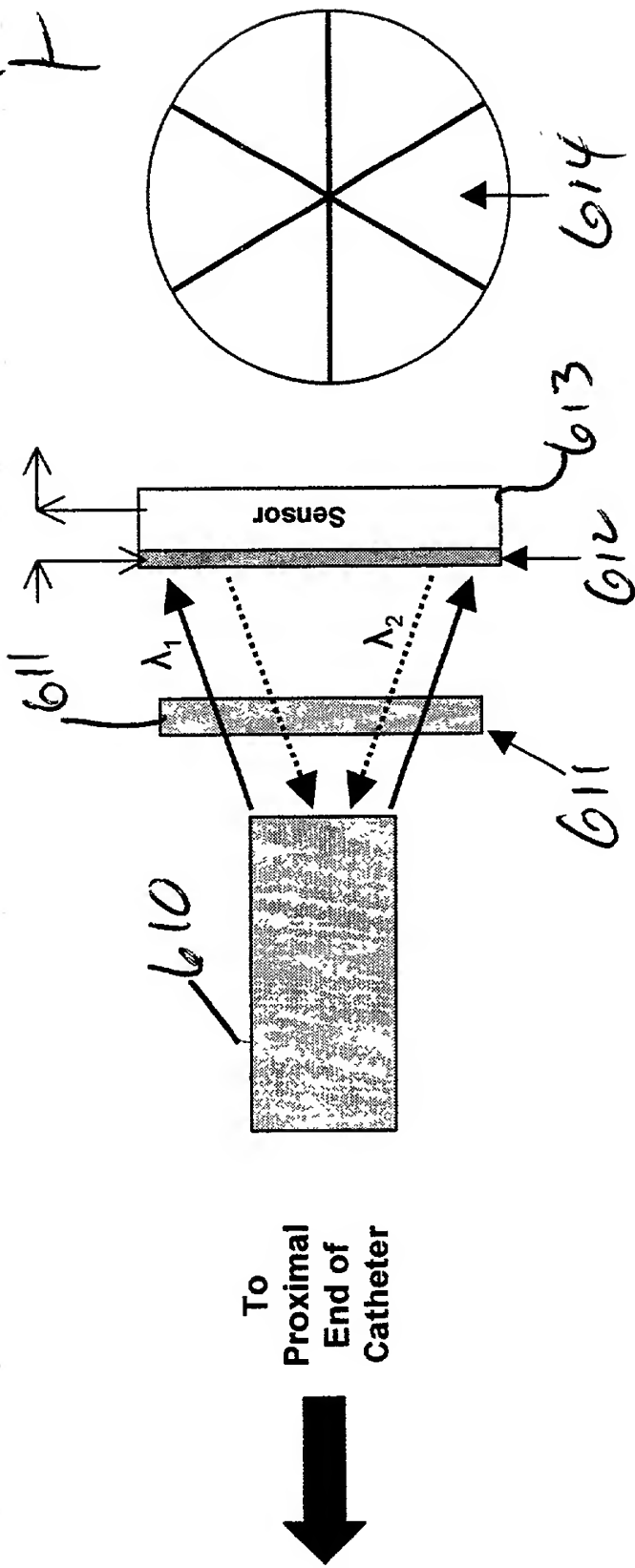
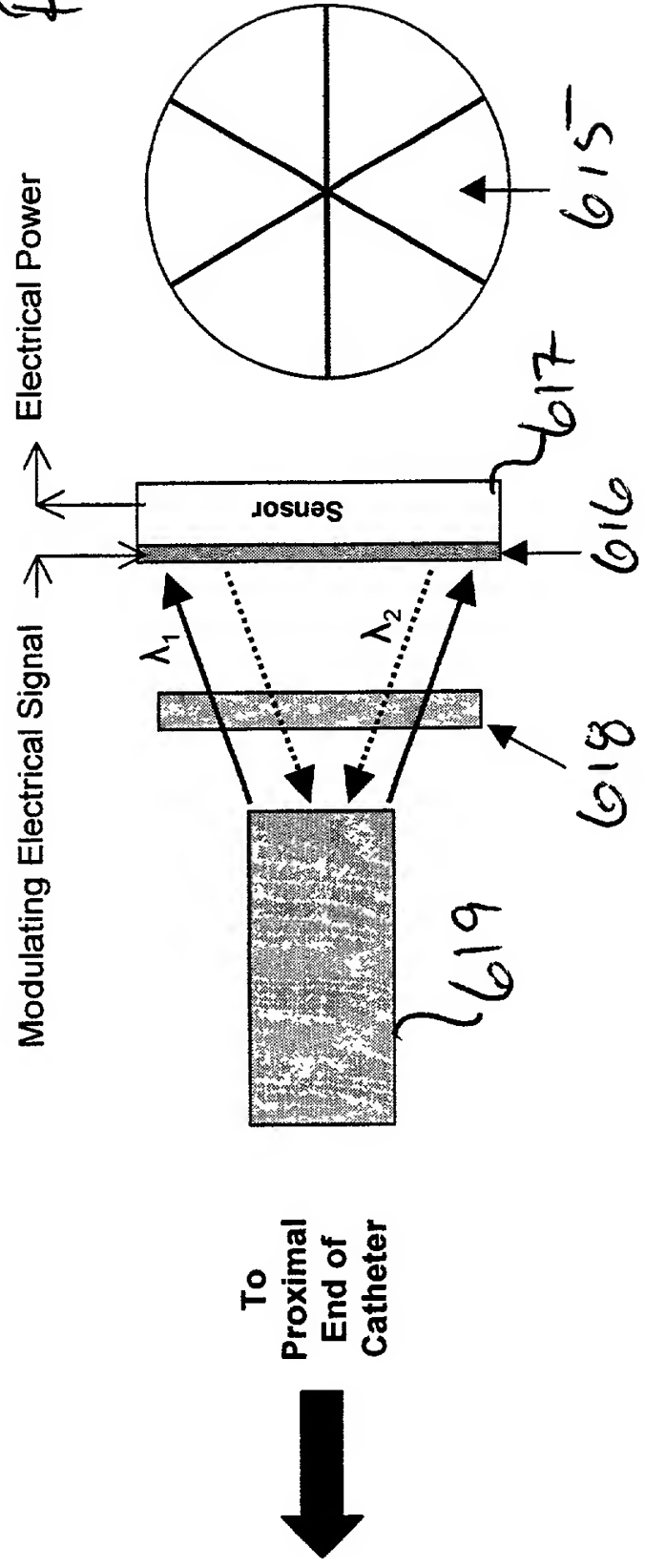
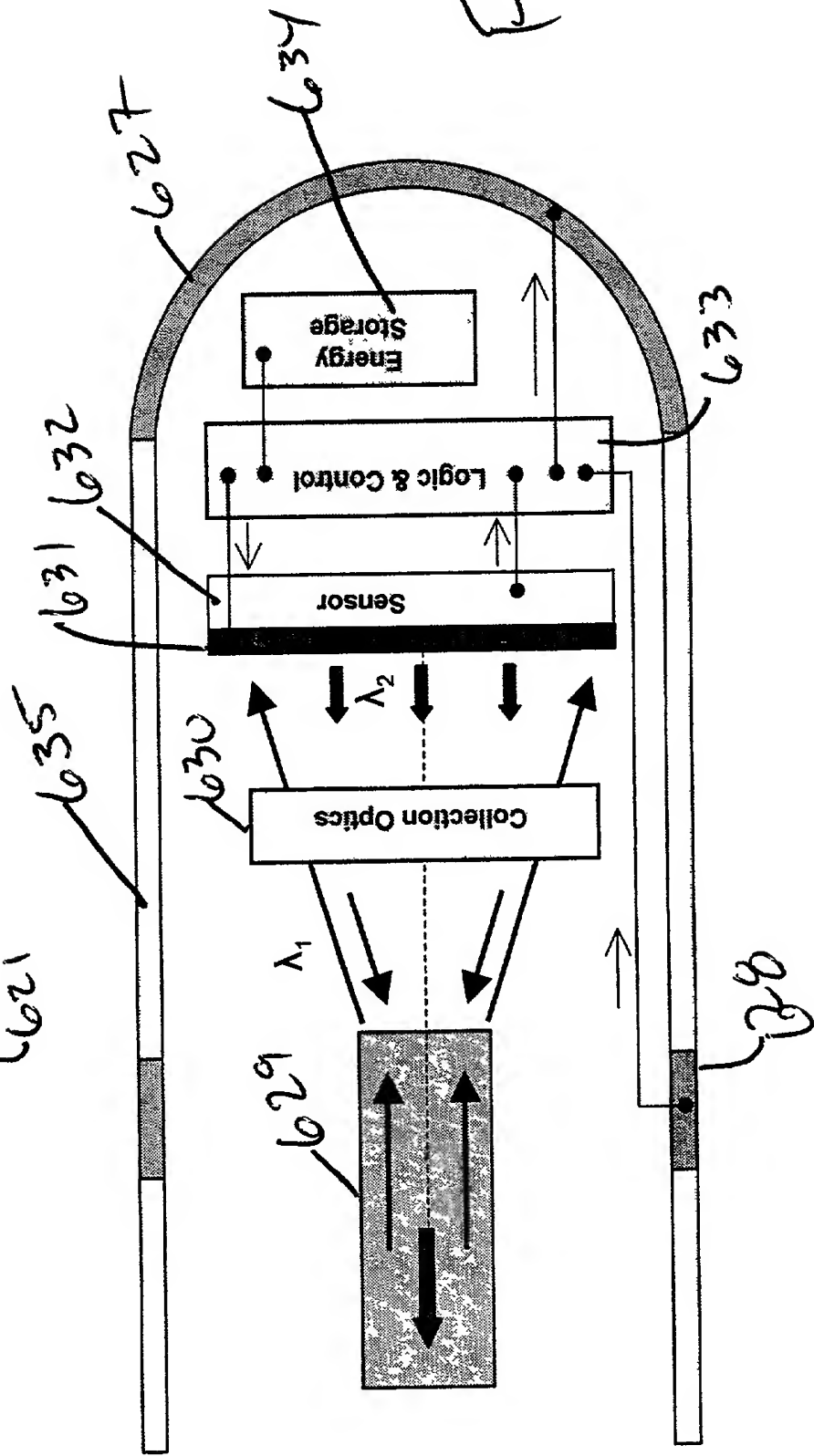
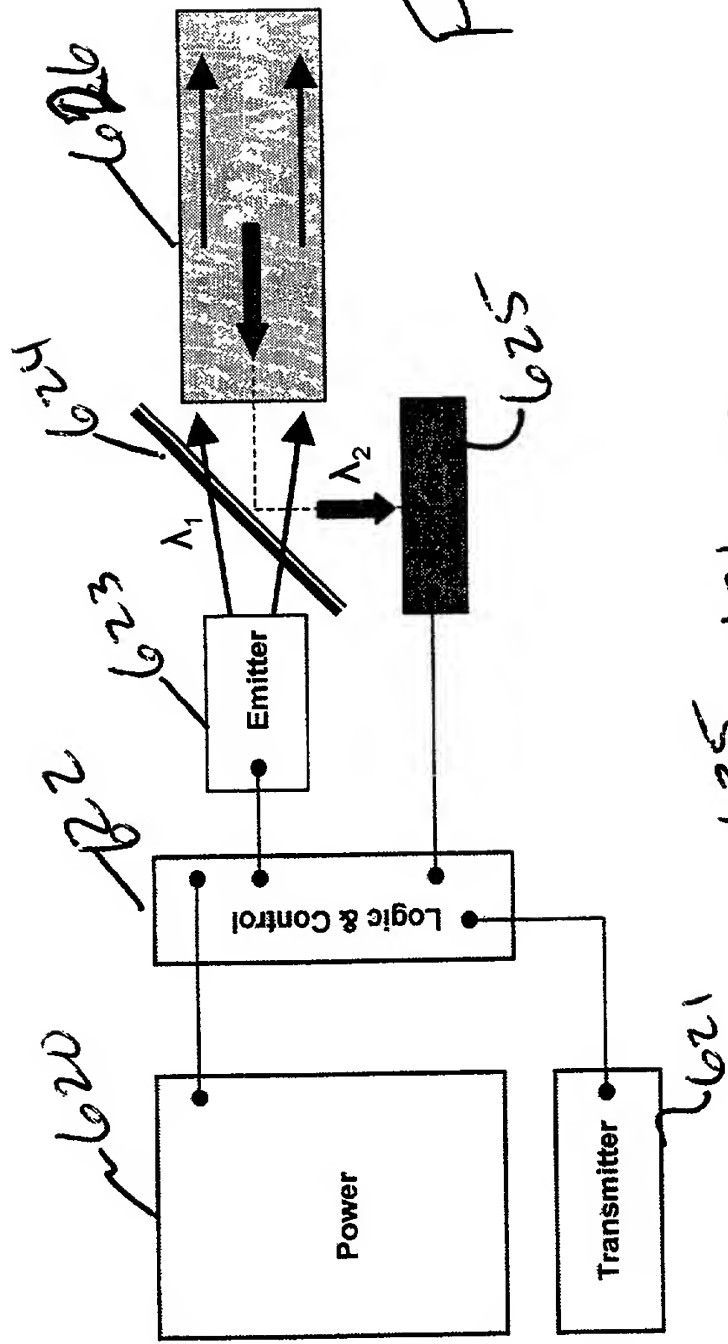


FIG 8





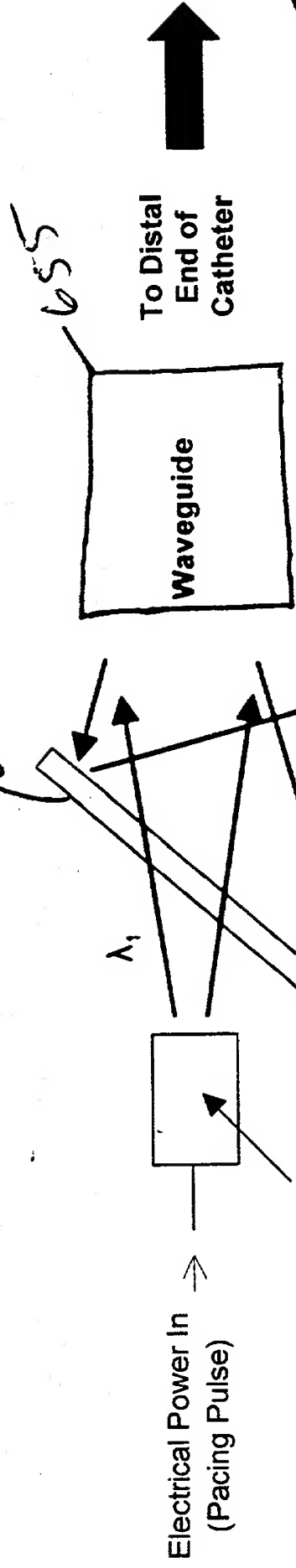
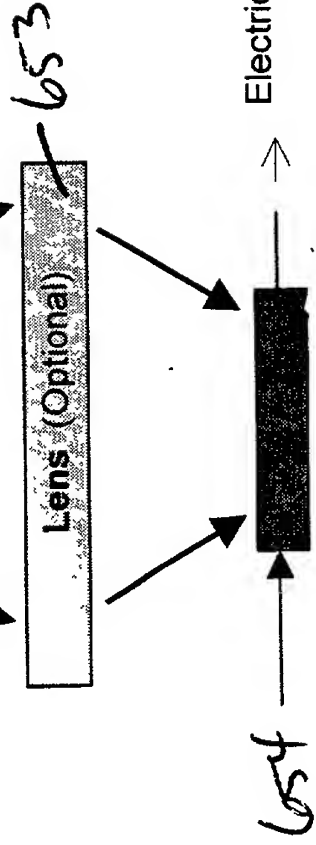
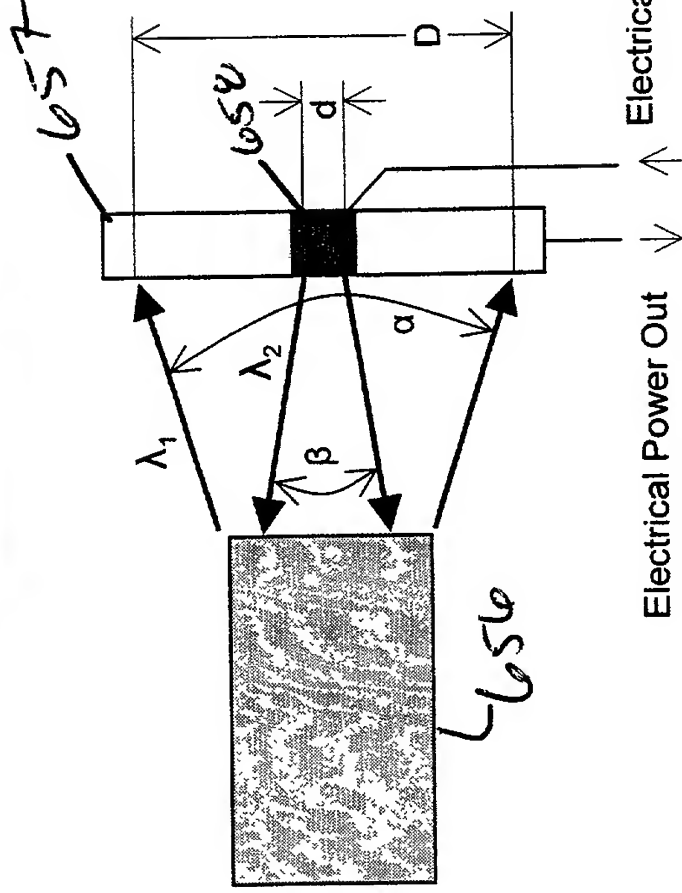


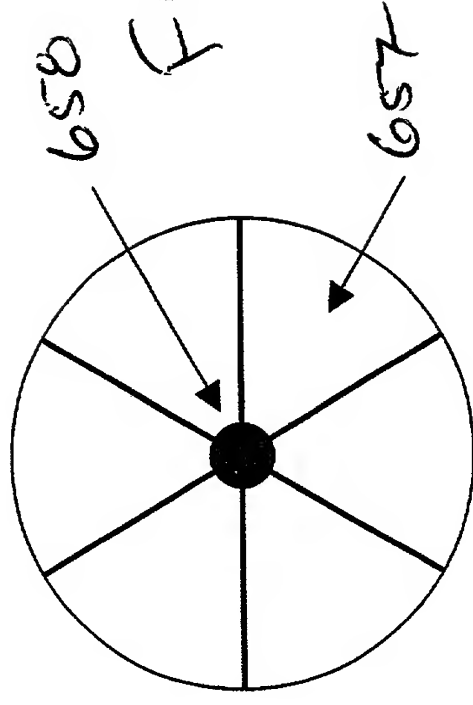
FIG. 13

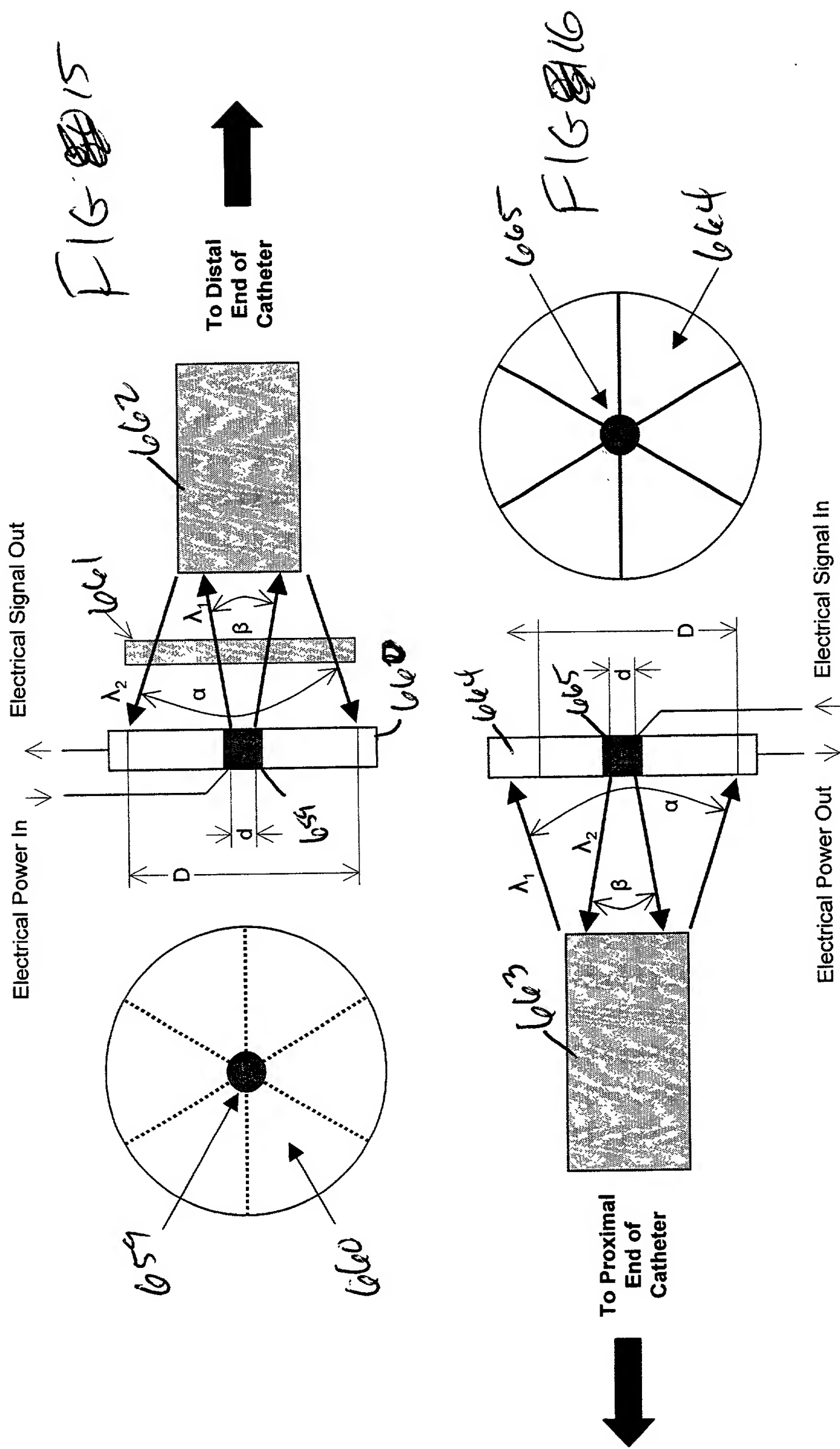


Electrical Signal Out



To Proximal End of Catheter





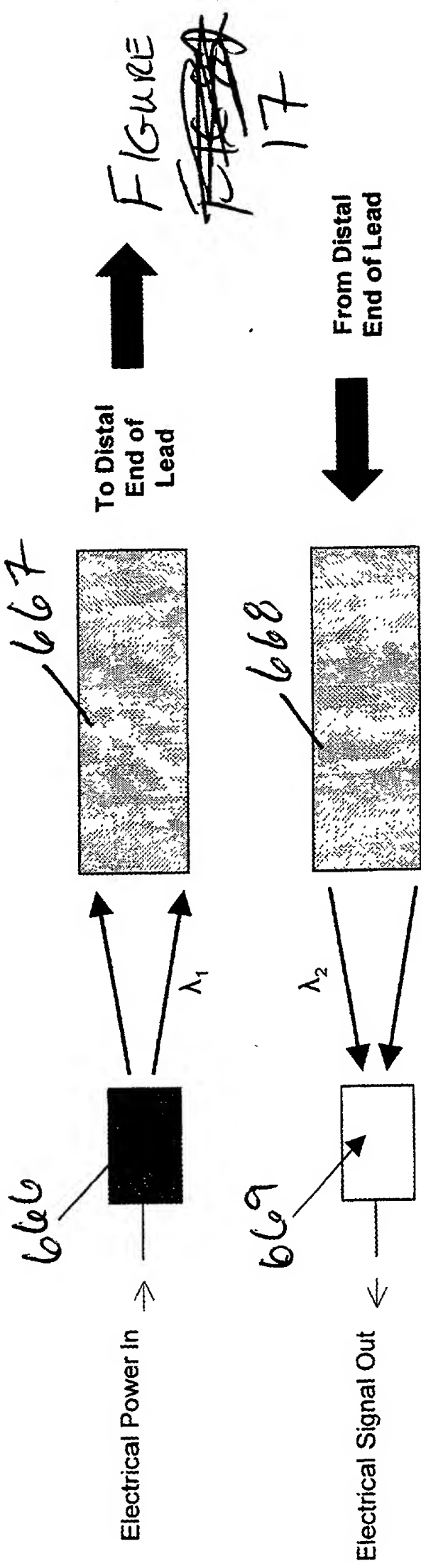


FIGURE
~~FIGURE~~
17

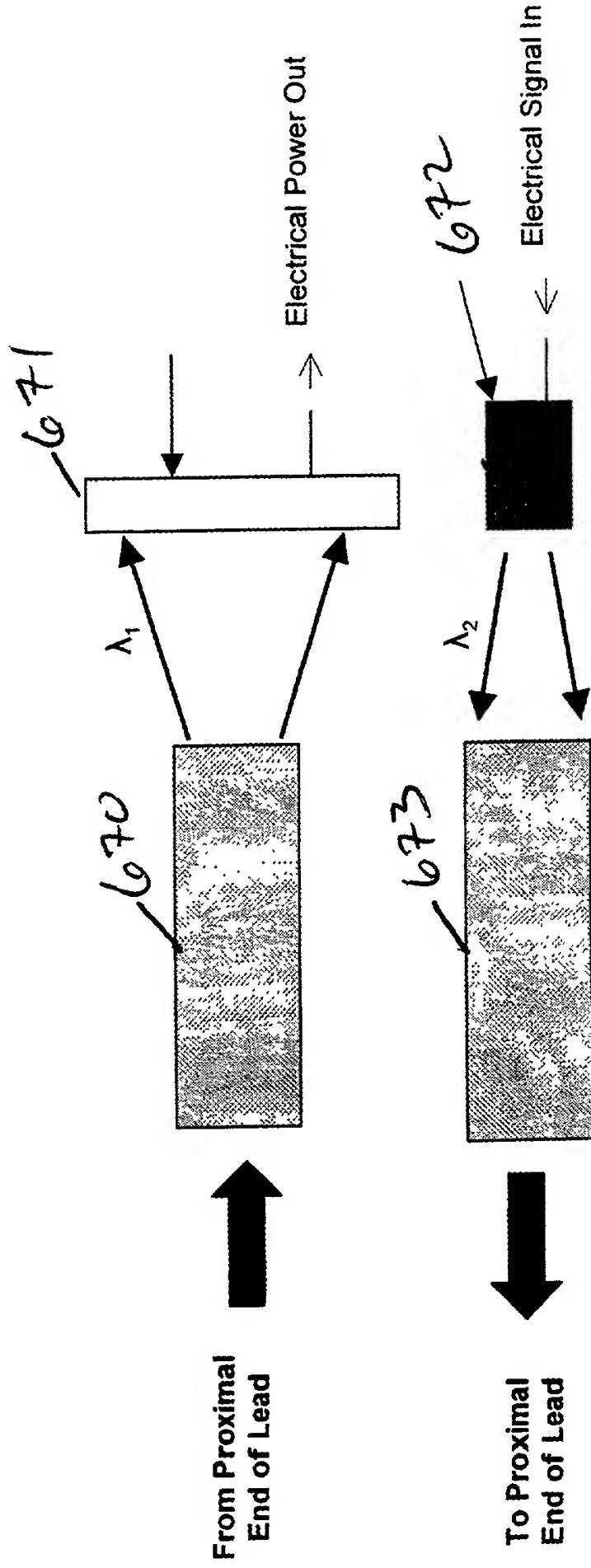
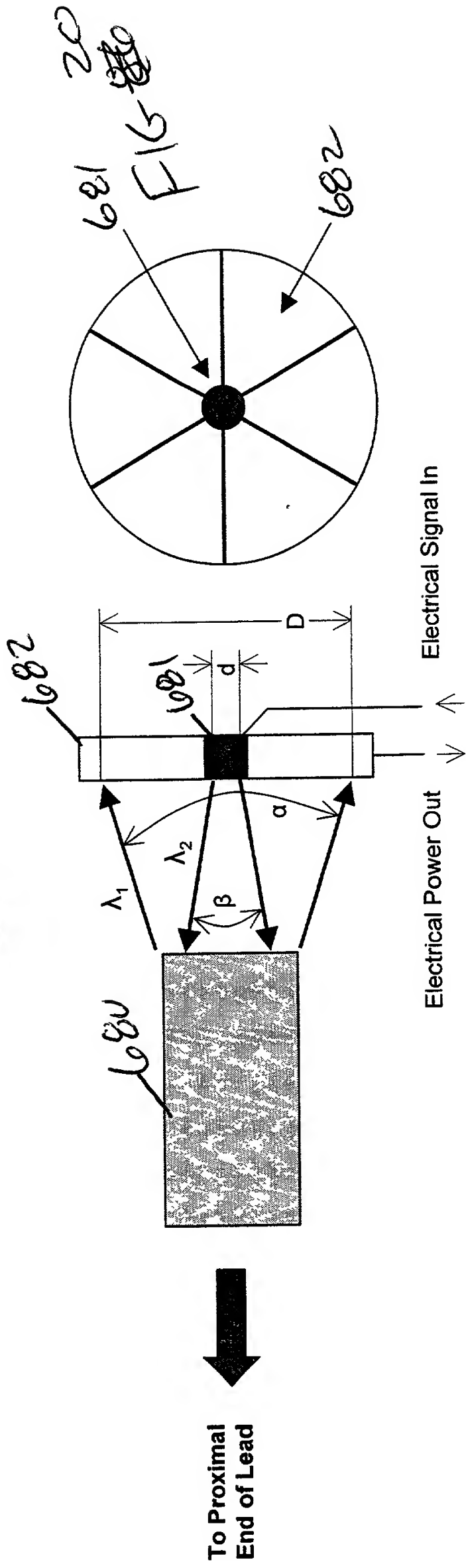
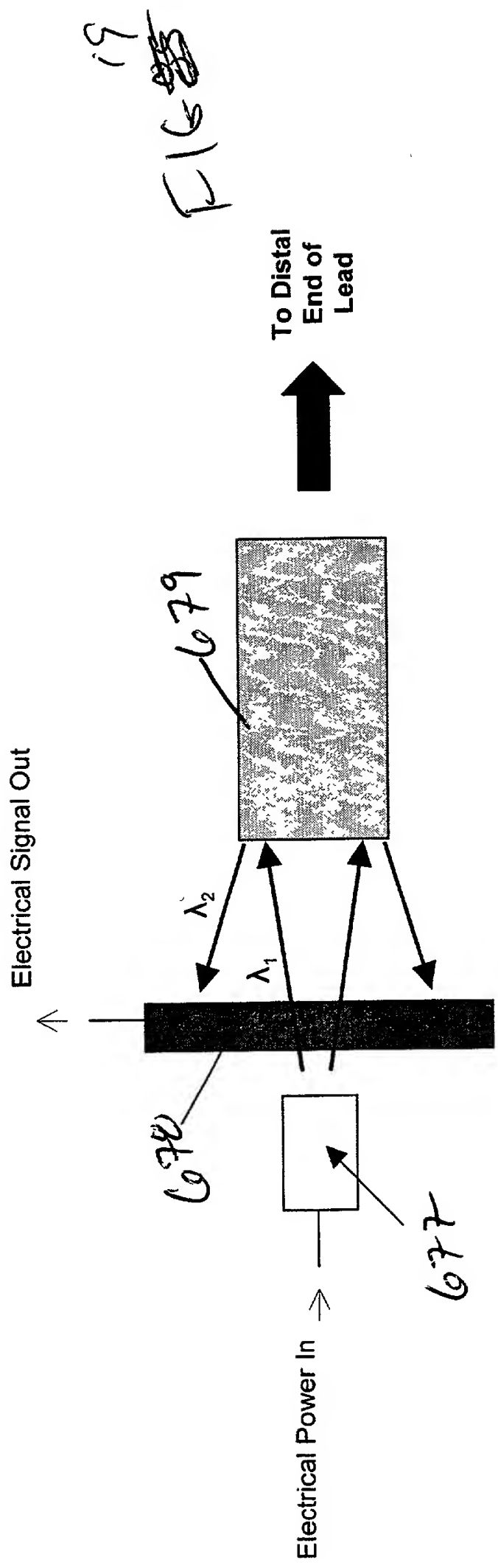


FIG
~~FIG~~
18



Fiber-optic coupling

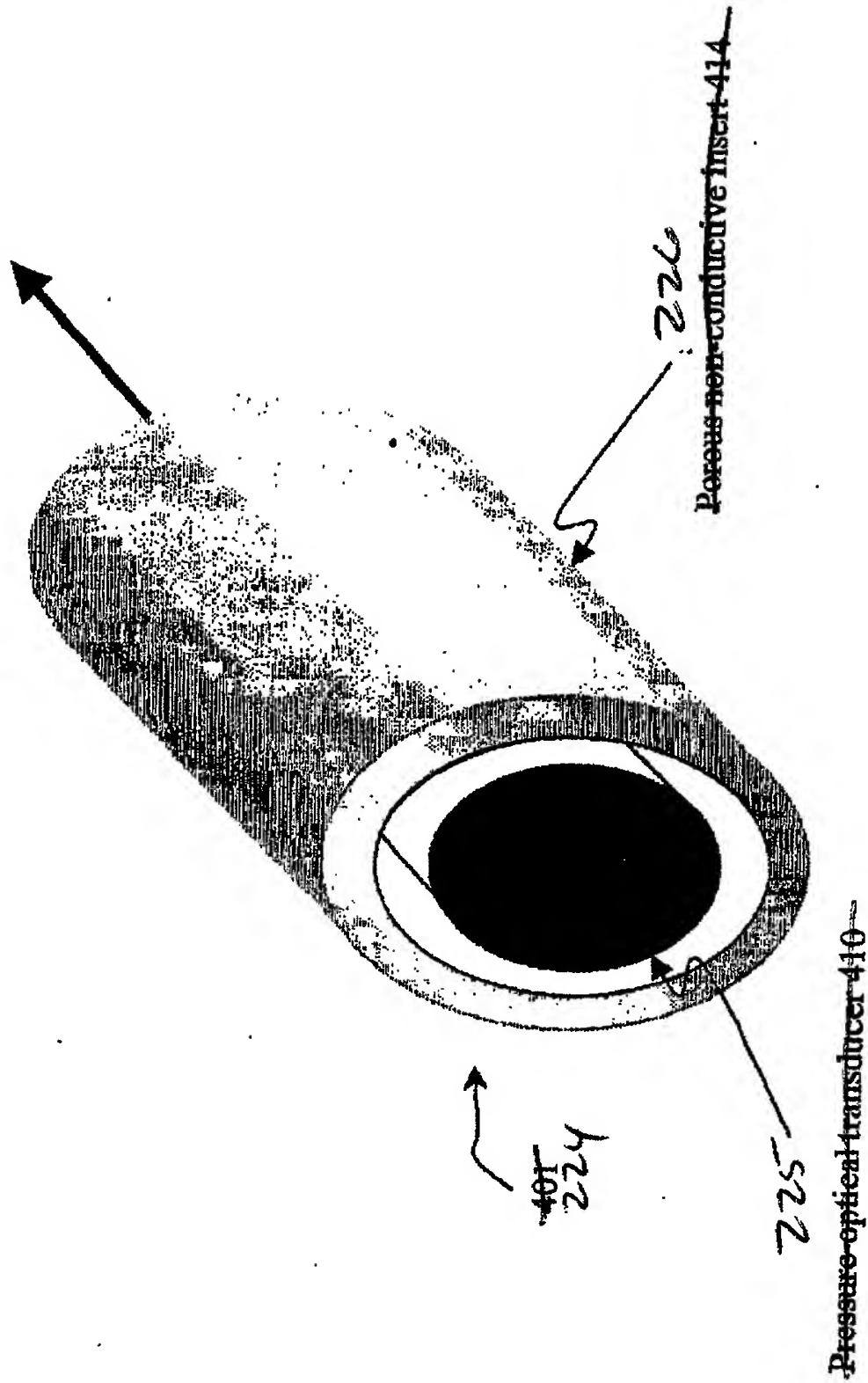
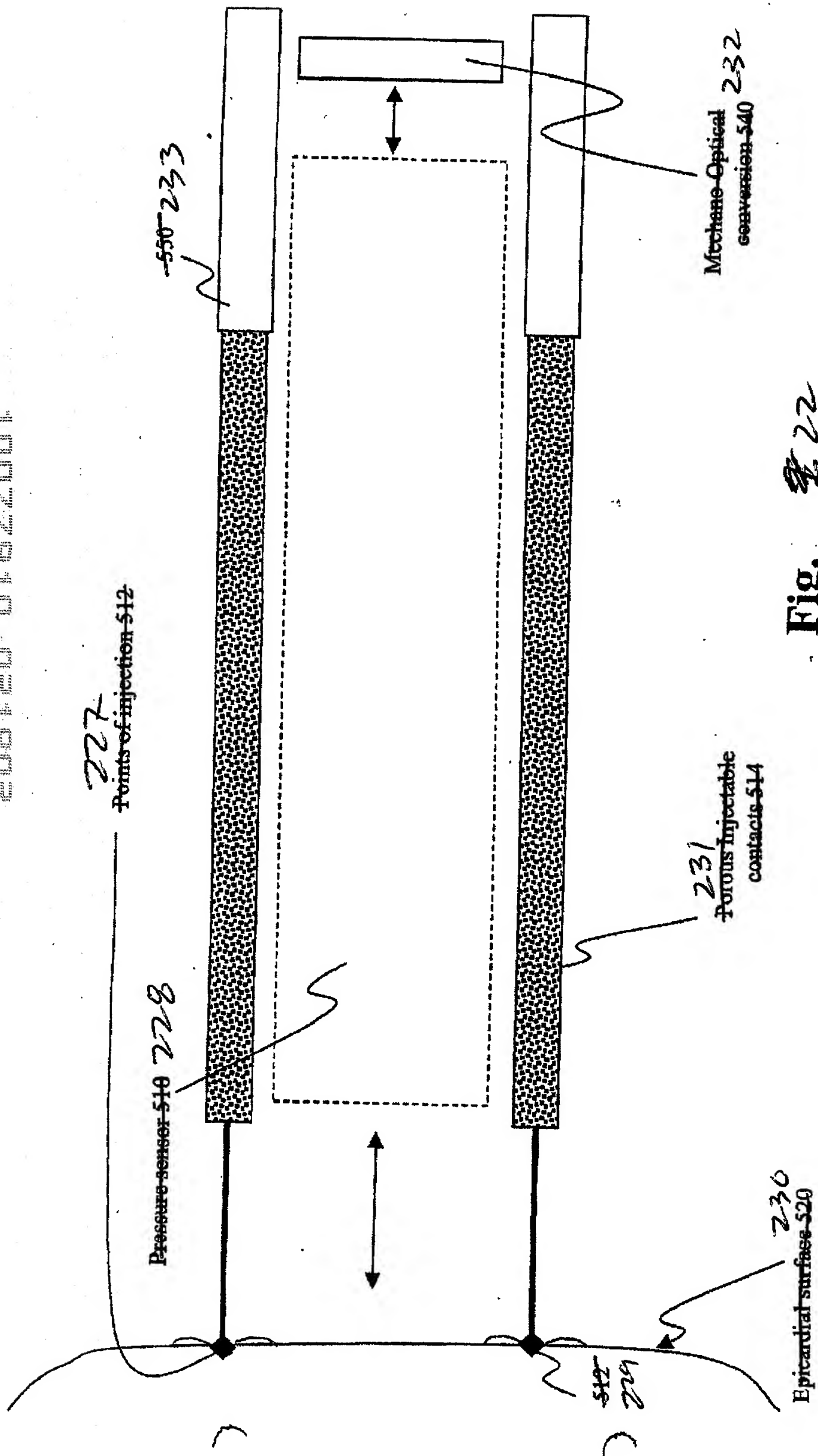


Fig. 12



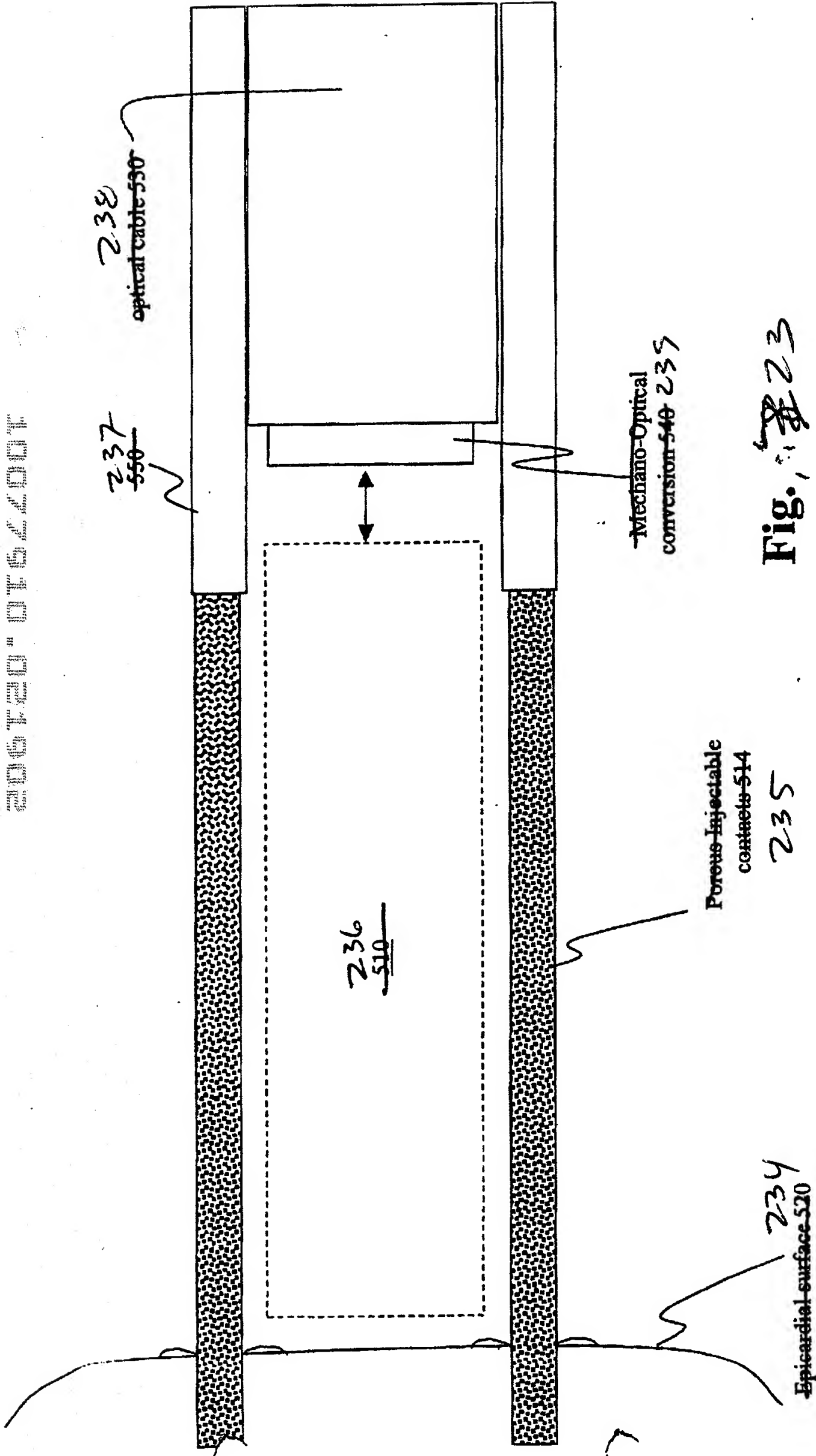


Fig. 23

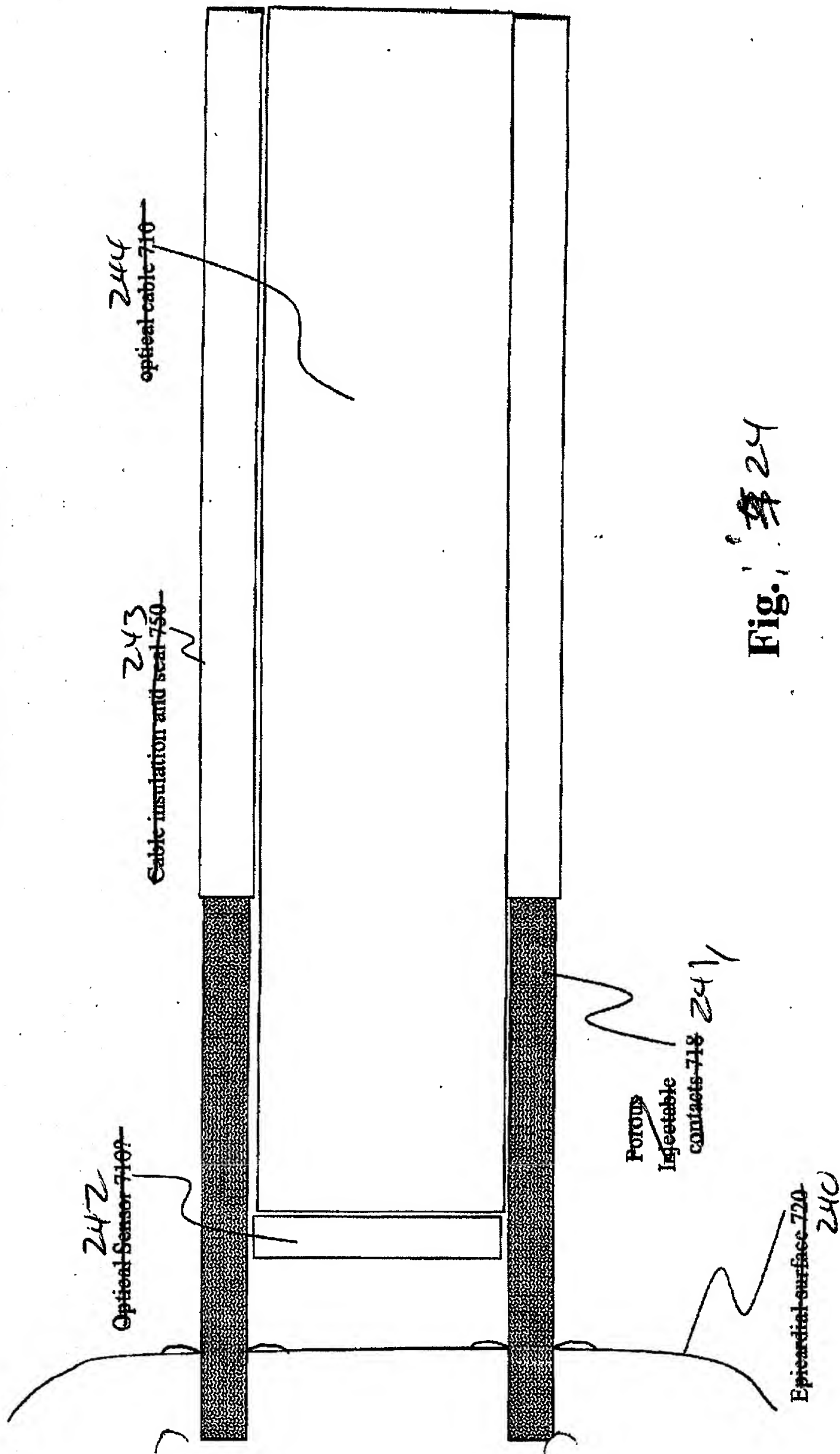


Fig. 1 第 24

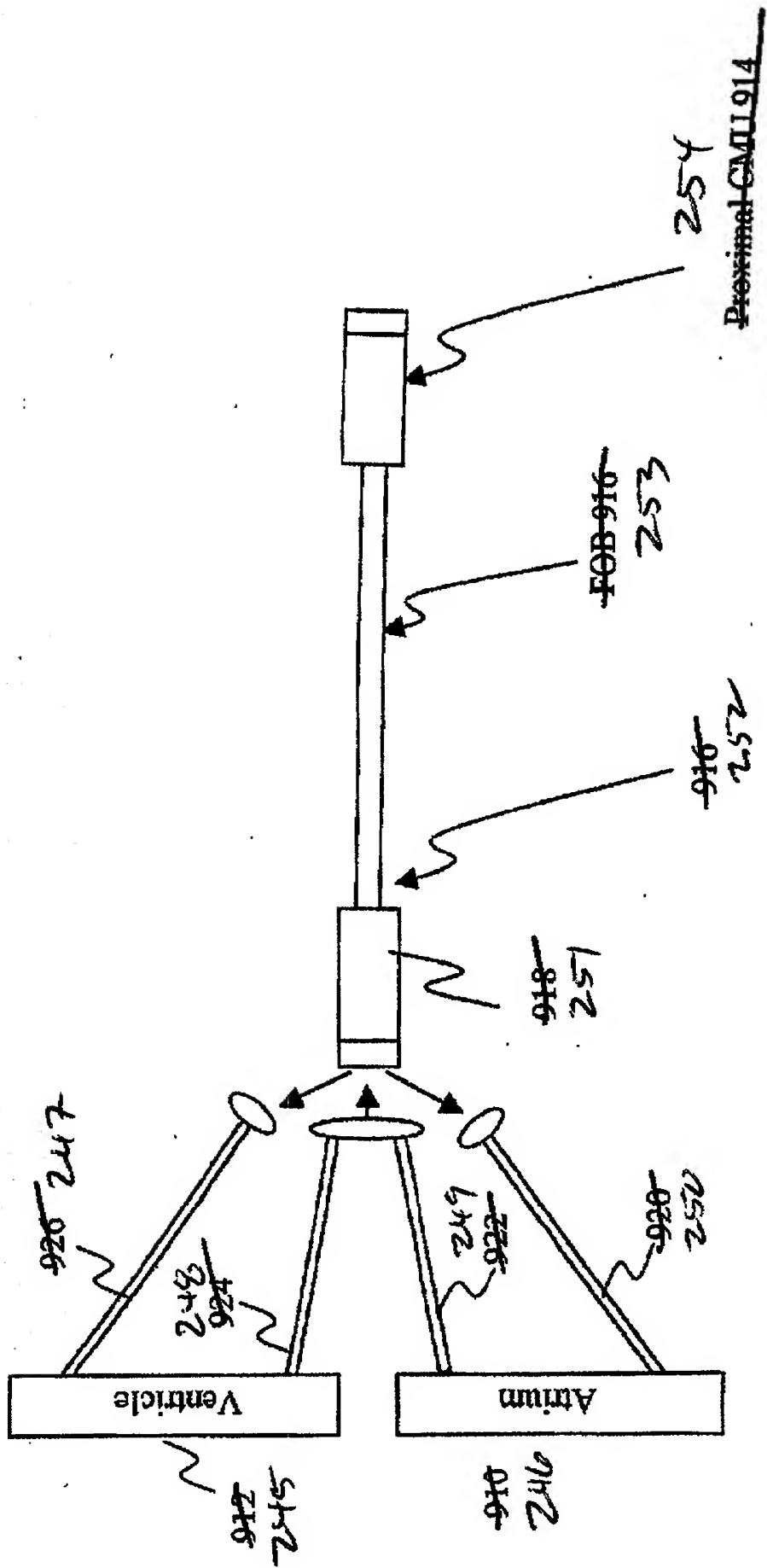


Fig. 1 ~~25~~

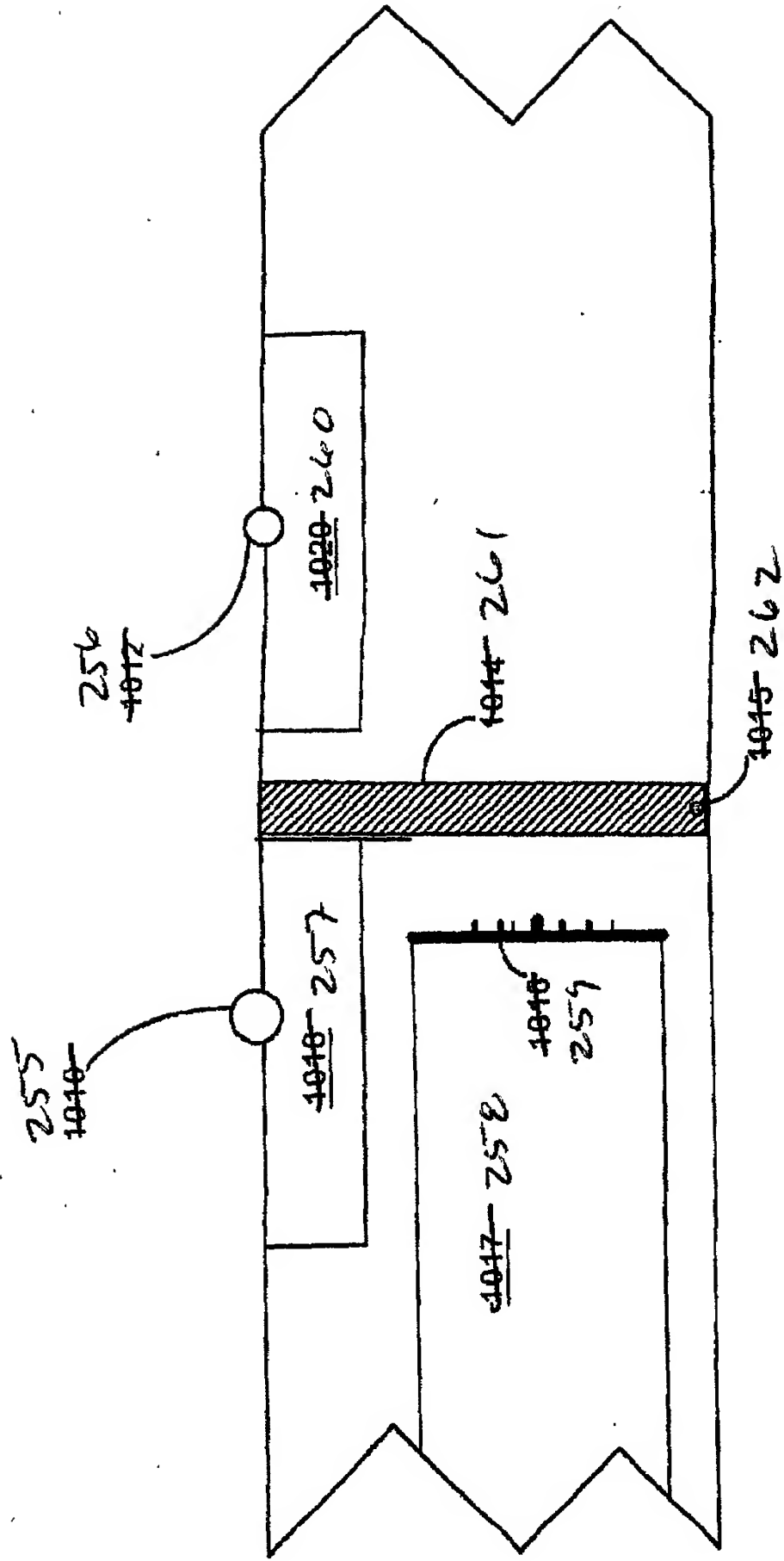


Fig. 26

#2

FIG 27

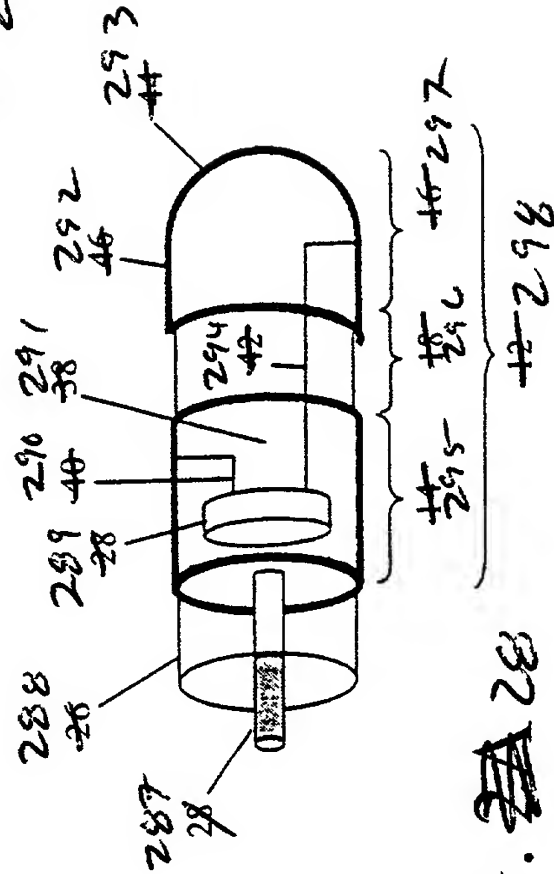
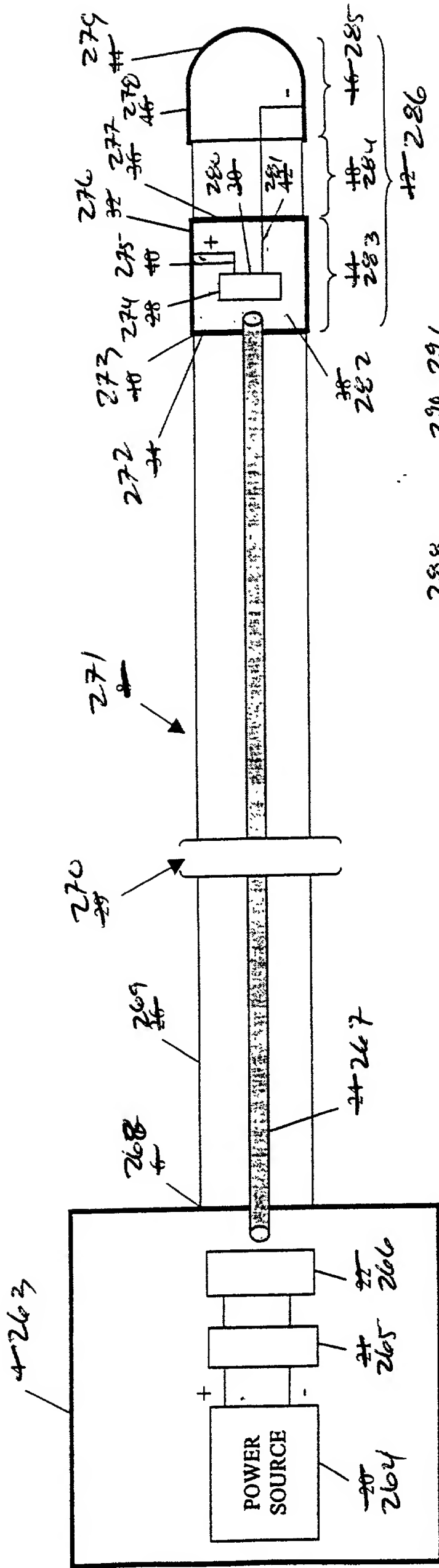


FIG. 28

FIG 2

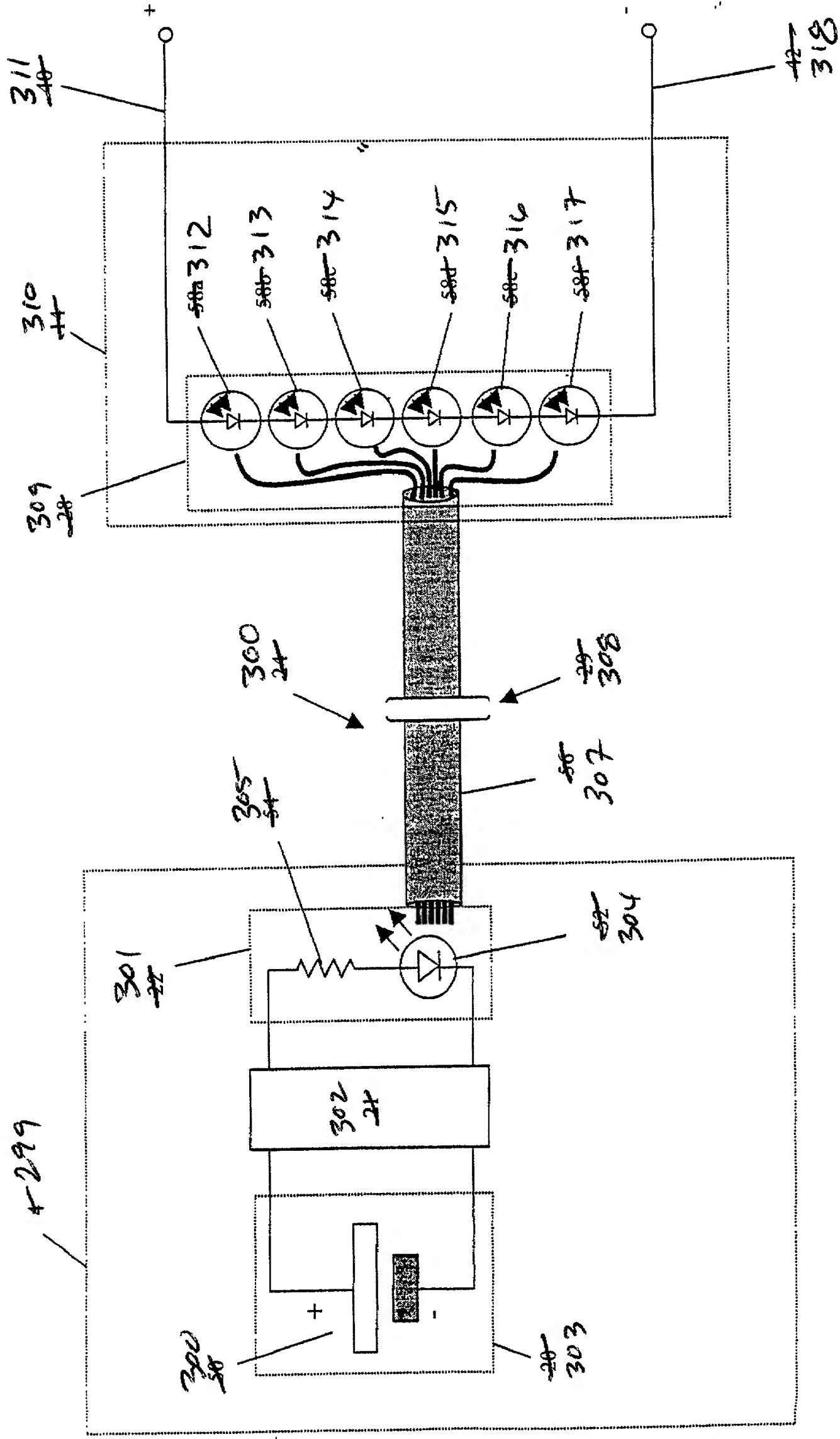


FIG. 29

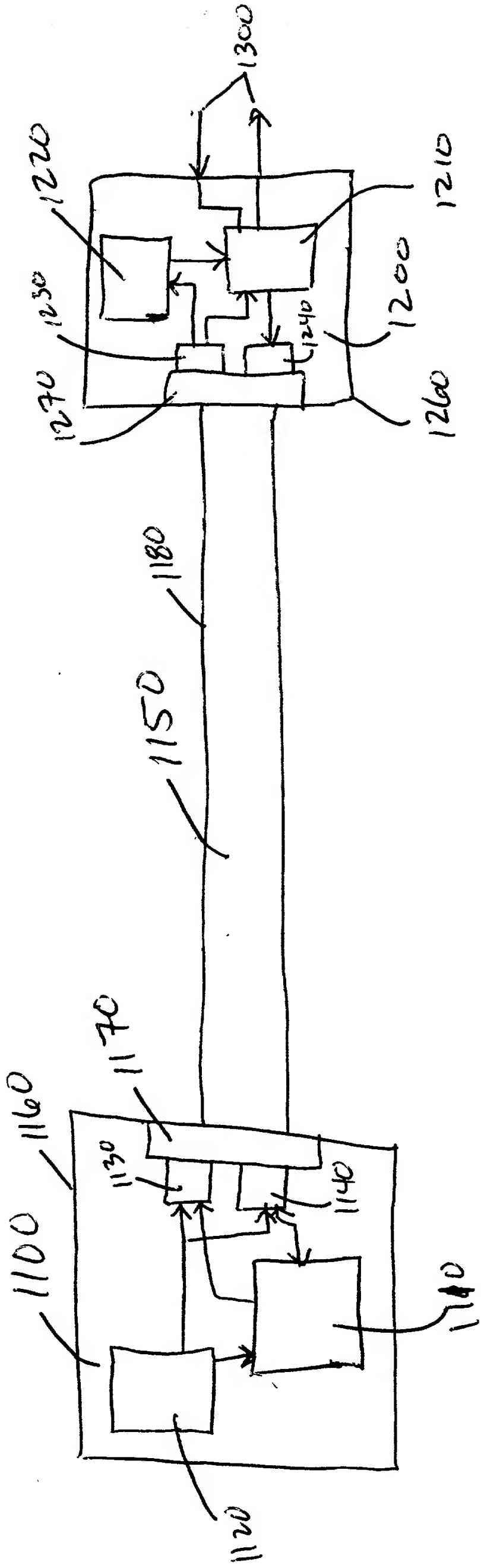


FIGURE 30

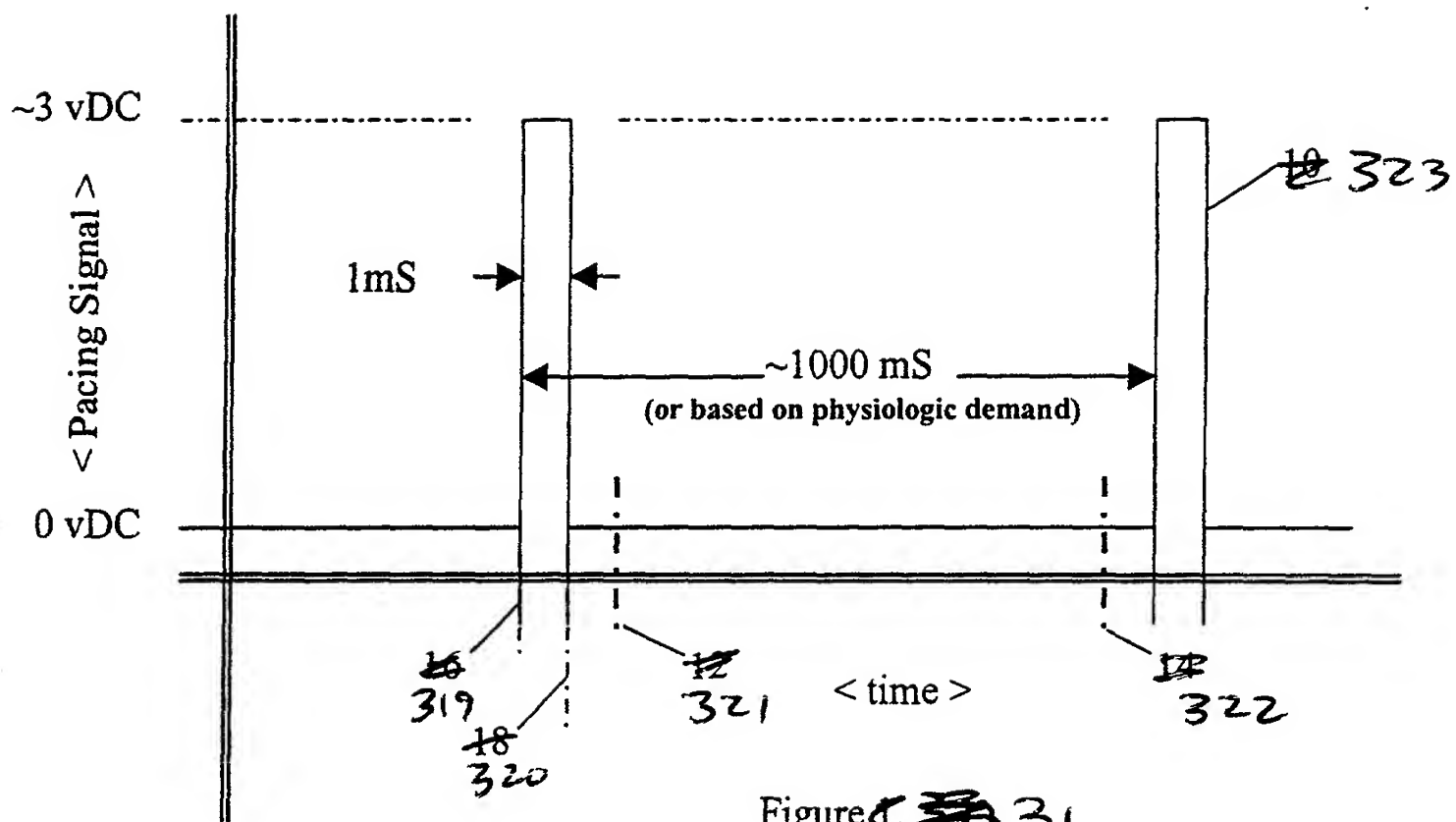


Figure 31

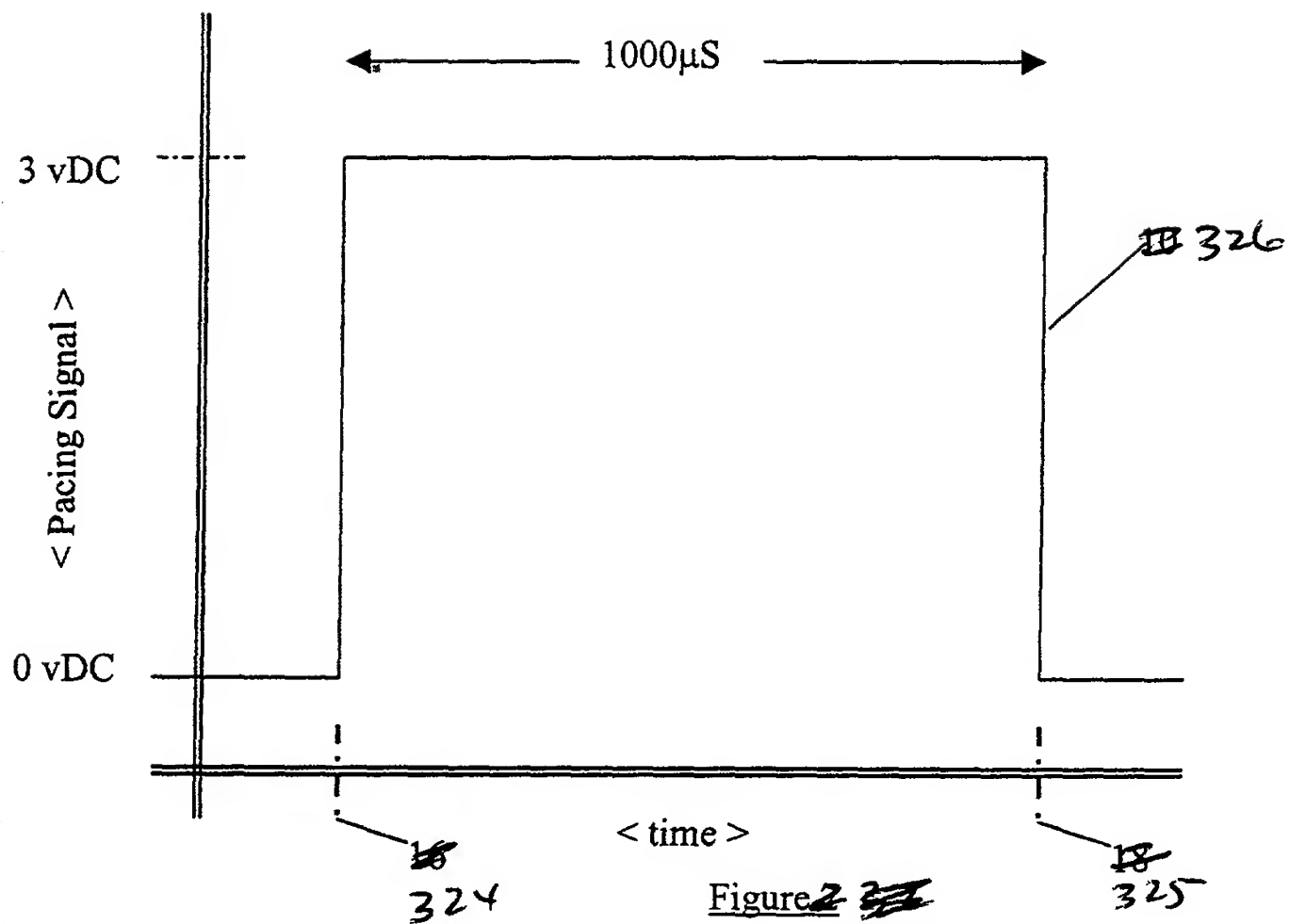


Figure 32



Figure ~~33~~ 33

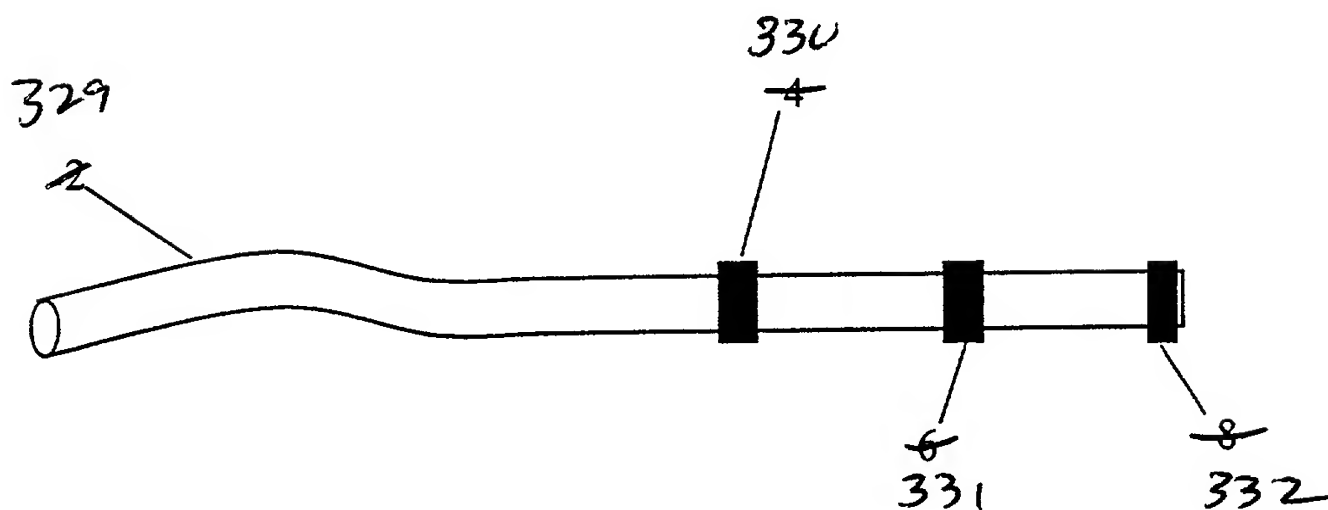


Figure ~~34~~ 34

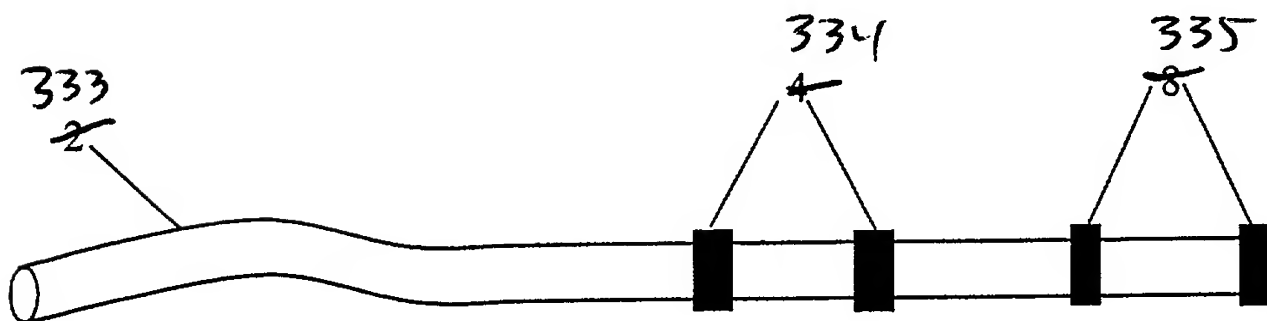


Figure ~~35~~ 35

2025-07-01 09:44:00

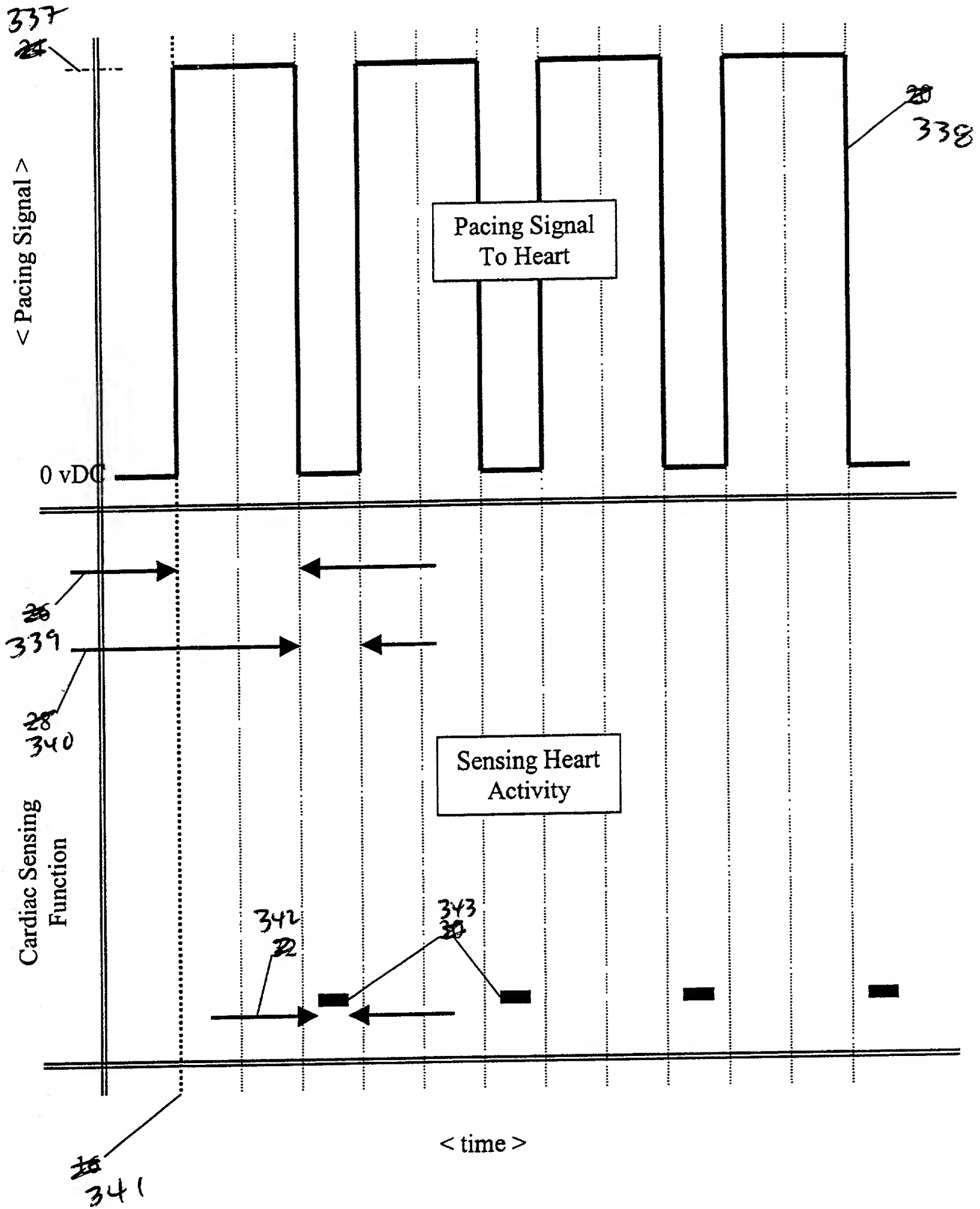


Figure 36

2025-01-09 10:44:00

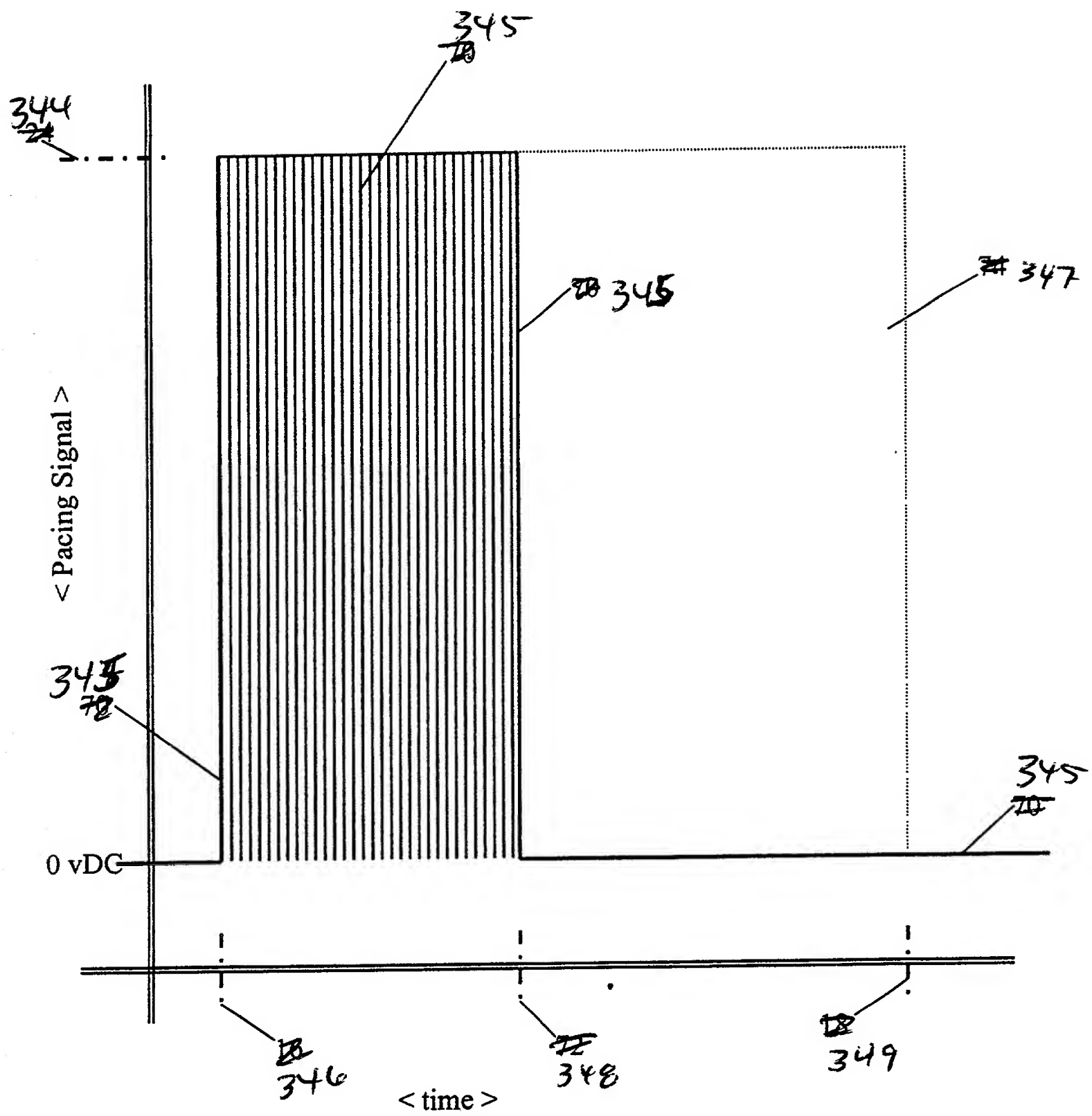


Figure 37

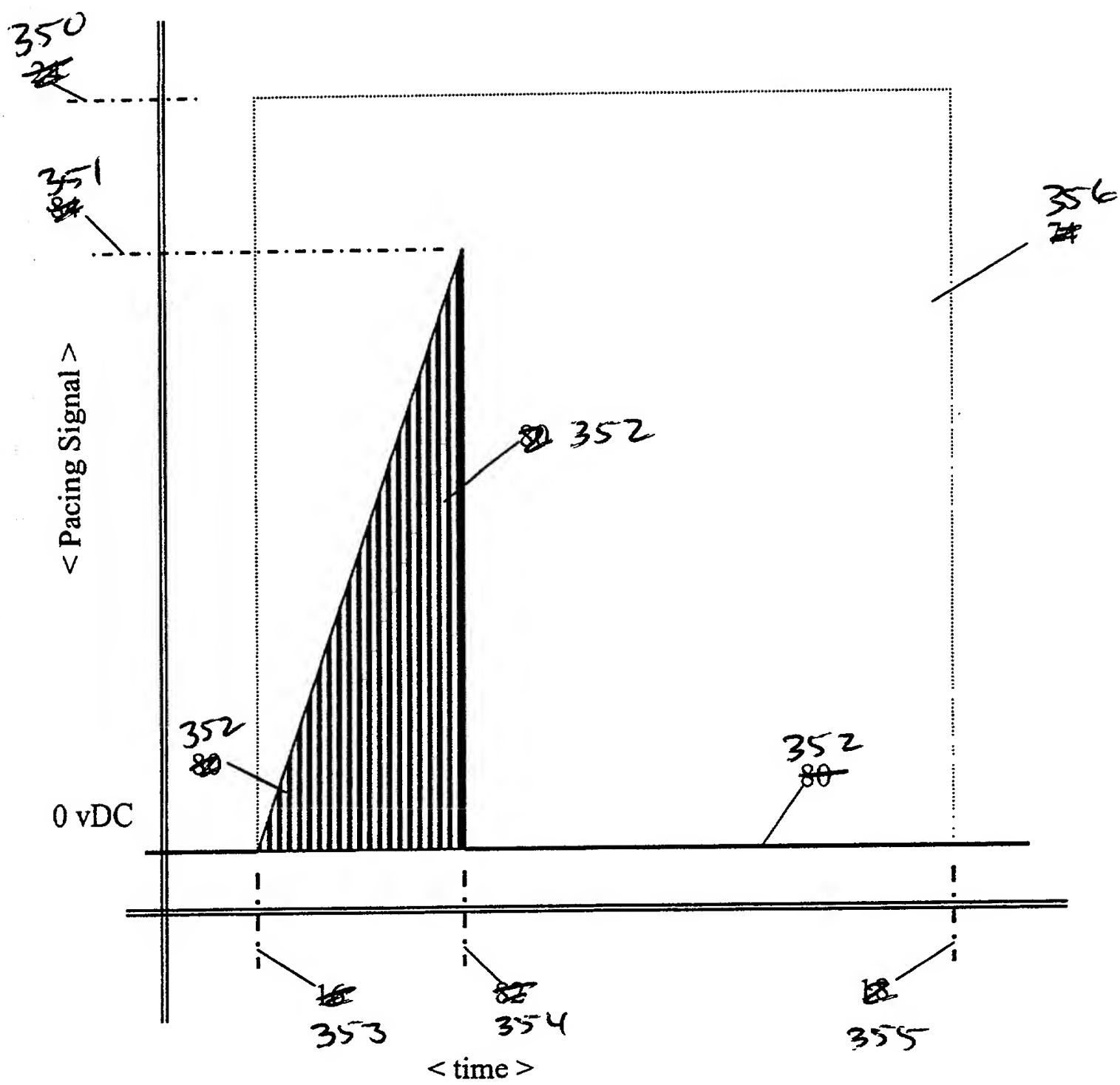


Figure 38

Timing diagram 360 showing a pacing signal over time. The signal starts at 0V, rises to a peak, and then falls back to 0V. The peak is labeled 360. The rising edge is labeled 361, the peak is labeled 362, and the falling edge is labeled 363. The signal is labeled 360. The diagram is labeled 360.

Figure ~~10~~ 39

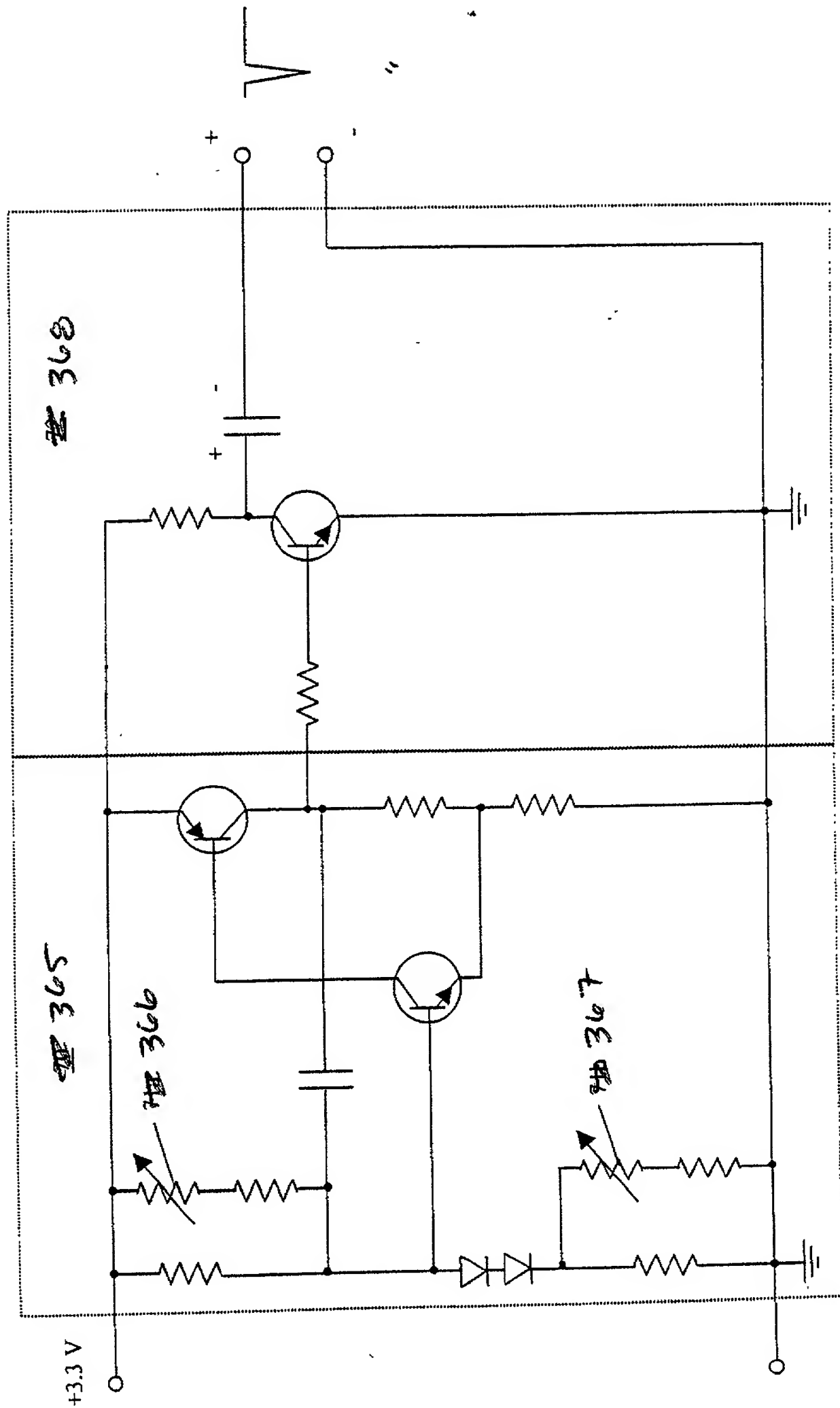


FIG. 40

369

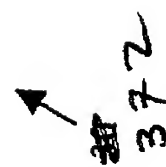
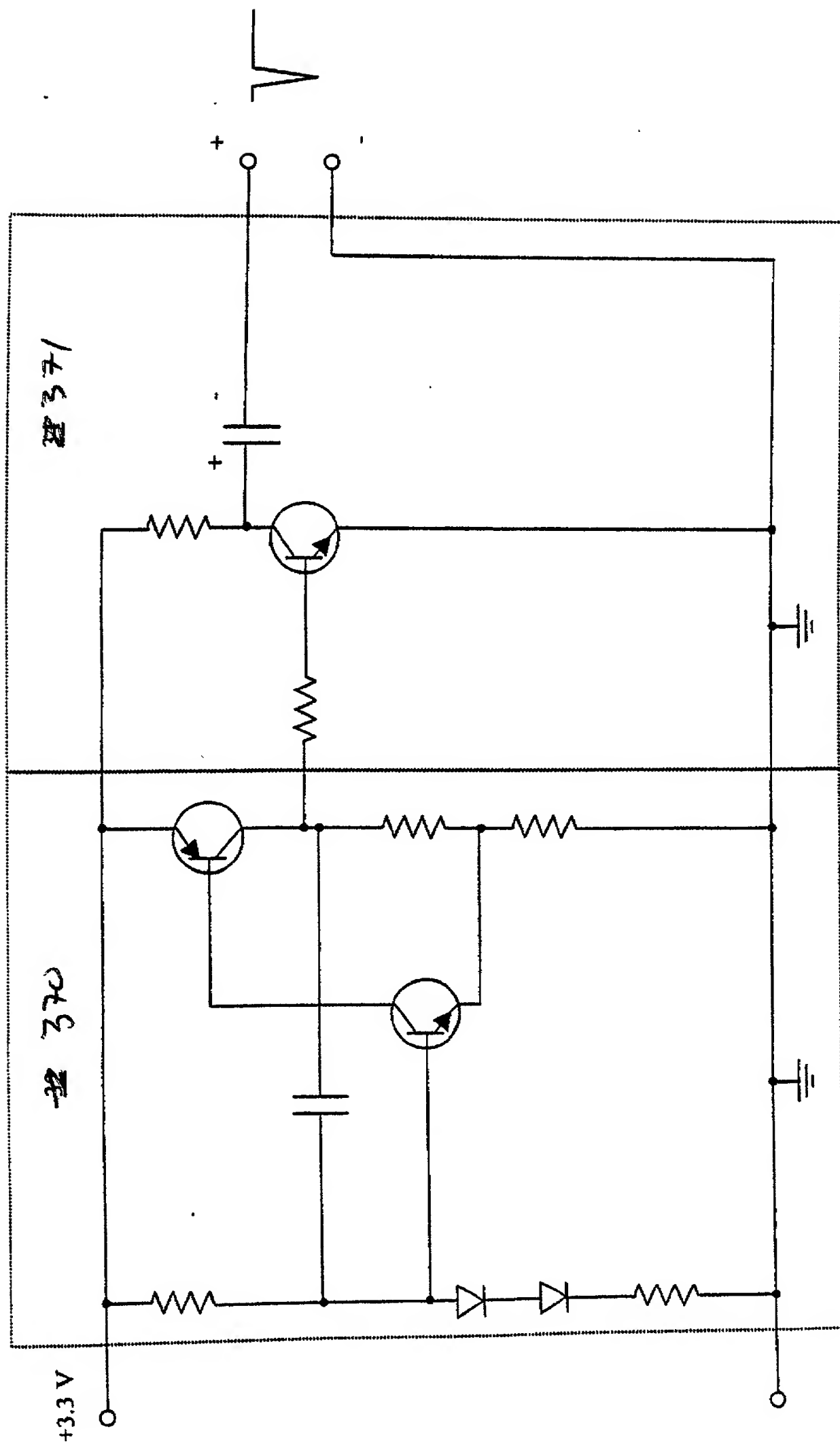
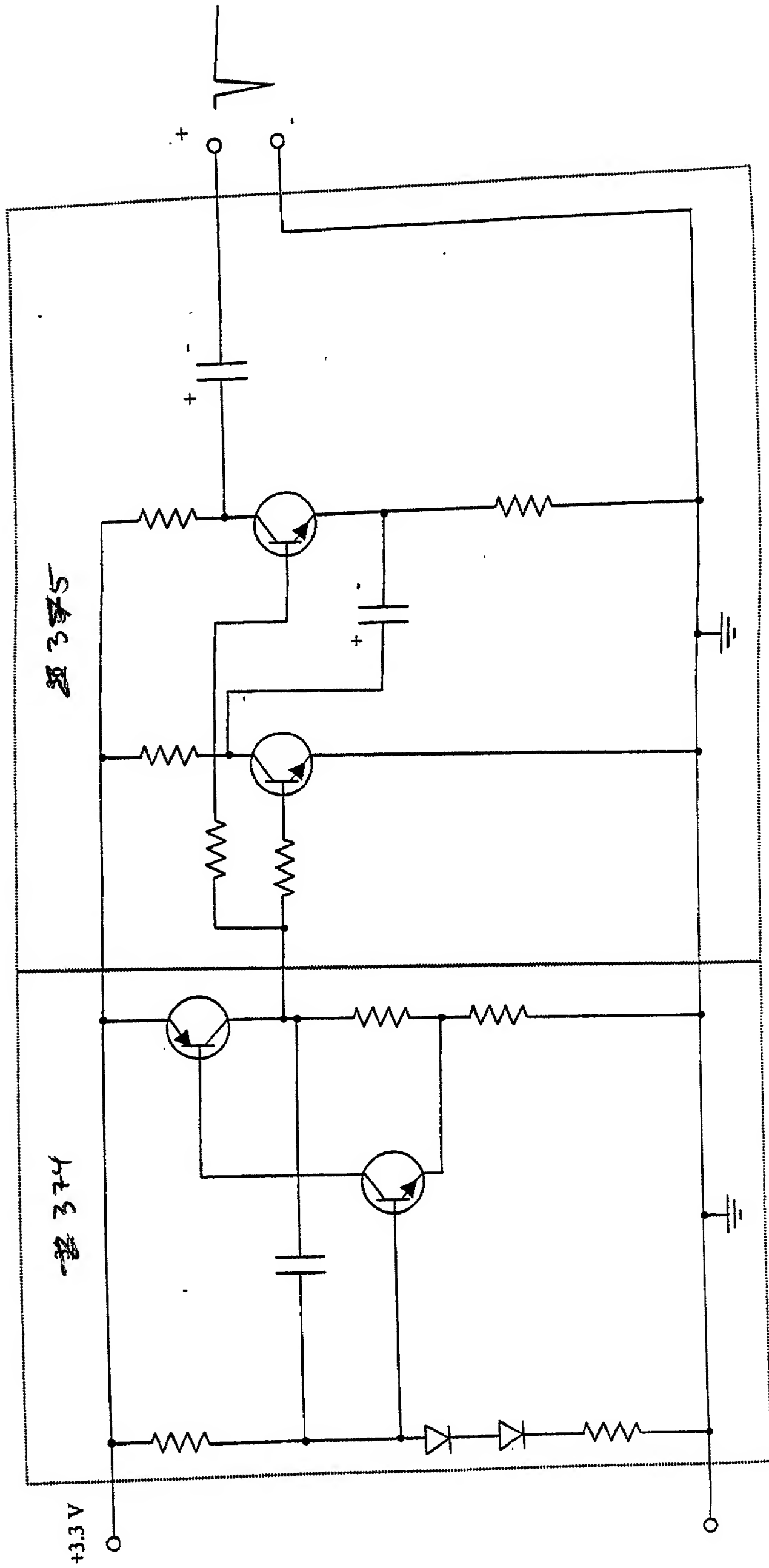


FIG. 341



373

FIG. 442

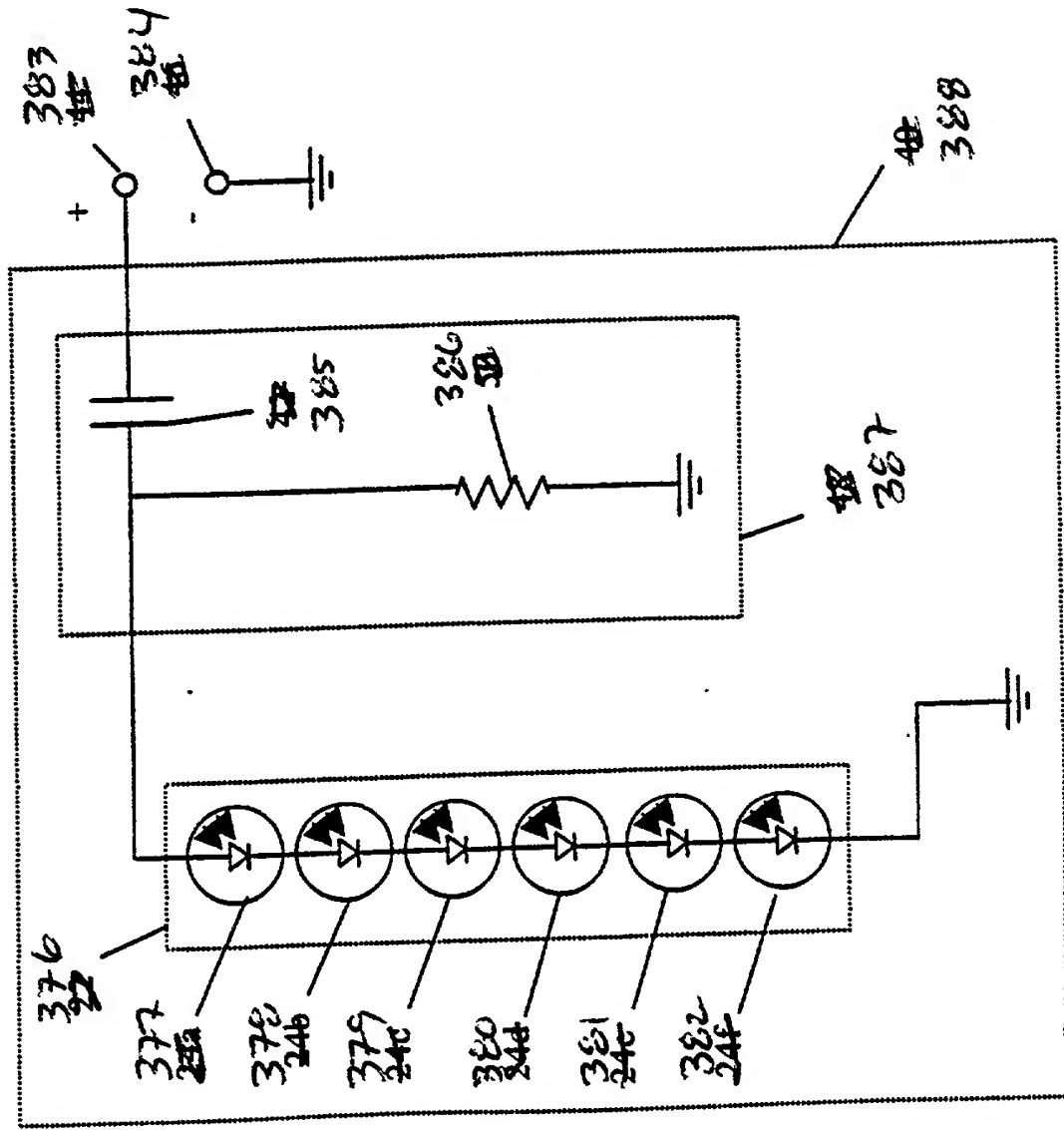
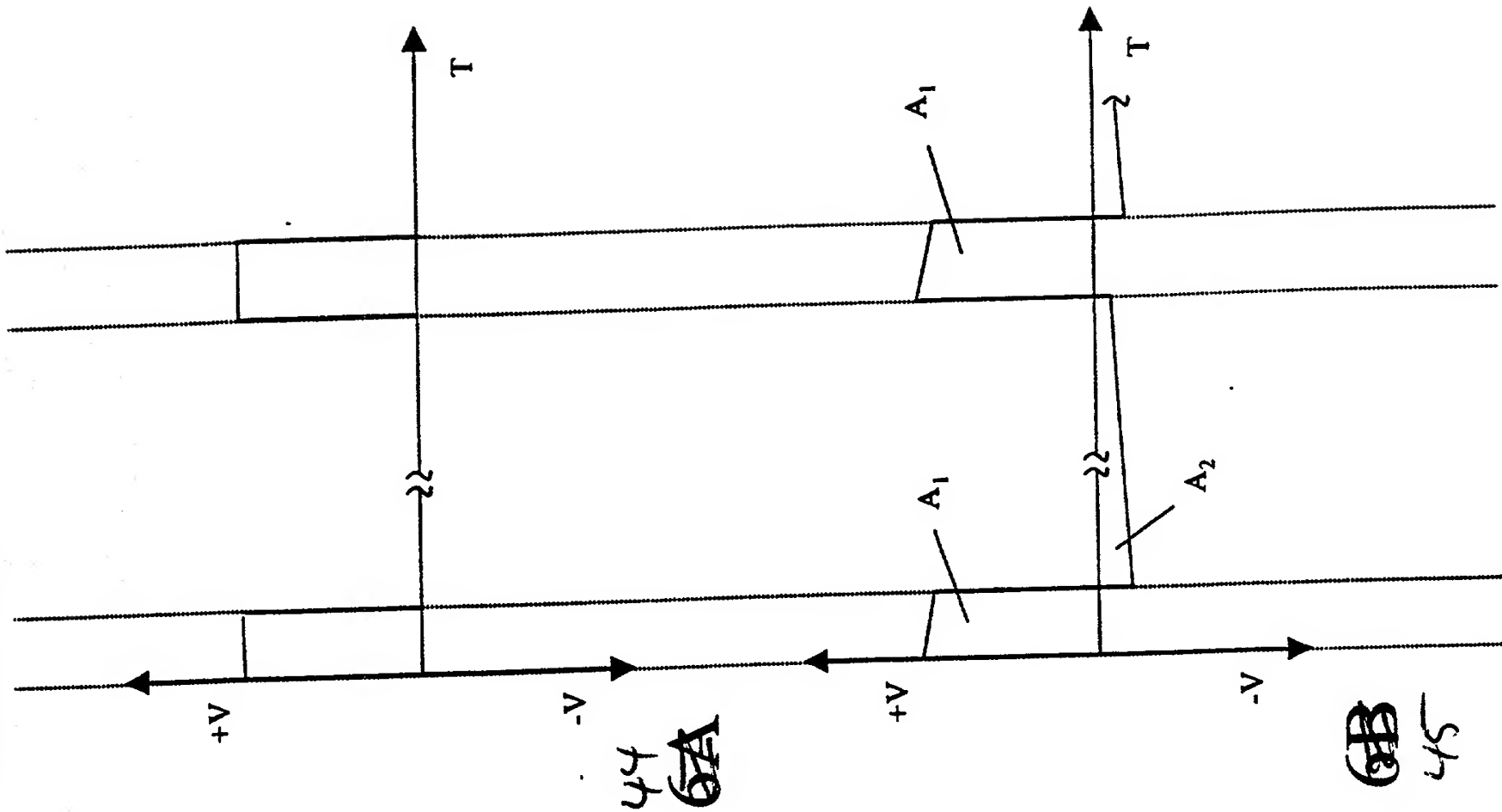


FIG. 8 43

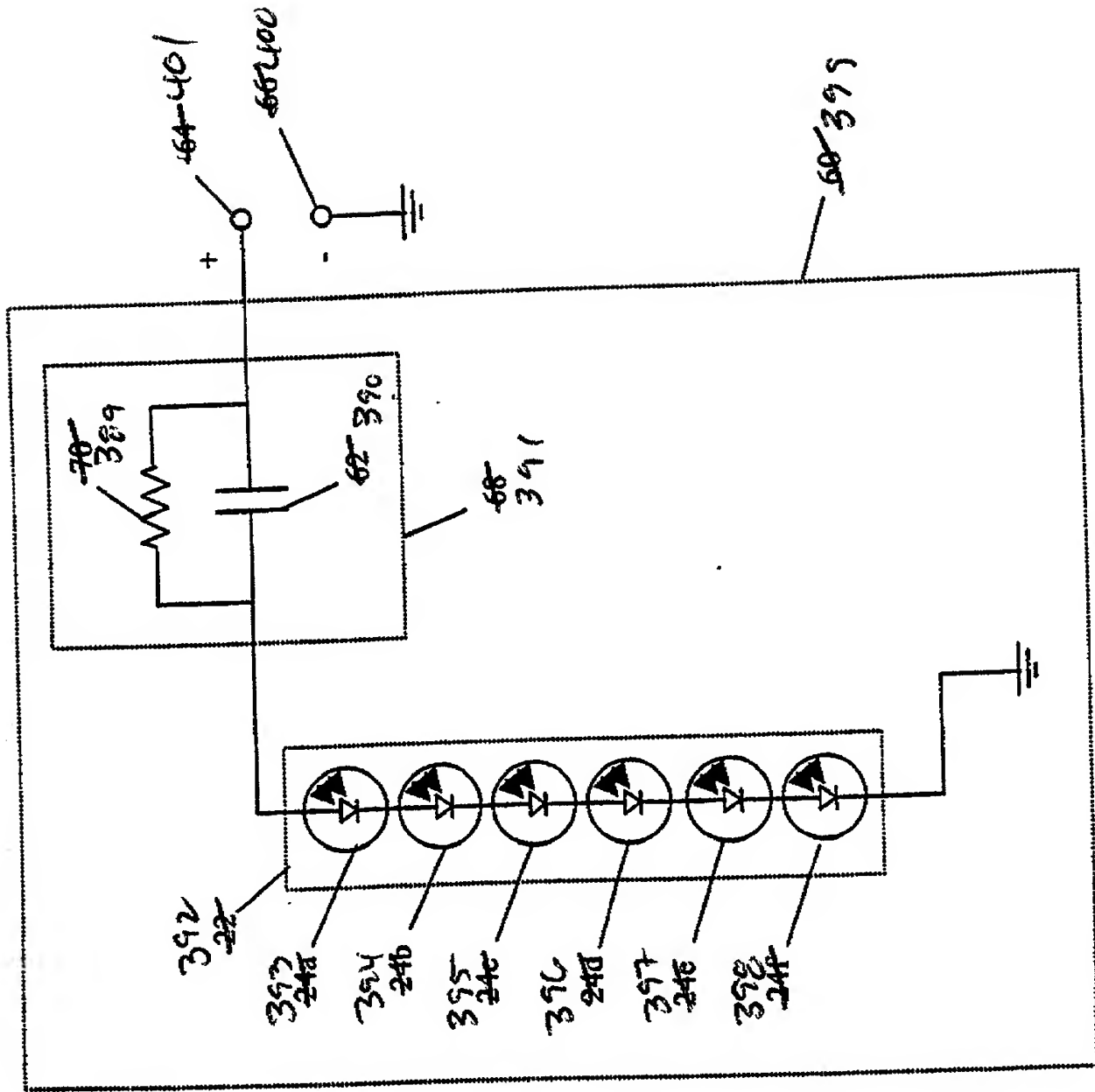


FIG. 740

The diagram illustrates a dual-channel electronic circuit, likely for signal processing or measurement. It features two identical processing channels, each consisting of a detector, amplifier, and output stage.

- Input Stage:** Each channel begins with an input terminal (425, 426) connected to a resistor (424, 427) and a capacitor (428, 429). The signal is then fed into a detector or discriminator stage (430, 431).
- Amplifier Stage:** The output of the detector is amplified by a multi-stage amplifier (432, 433).
- Output Stage:** The amplified signal is sent to an output terminal (434, 435).
- Control and Timing:** A central section labeled "PULSE GENERATOR" (436, 437) provides a common timing or control signal to both channels. It includes a pulse generator (438, 439) and a timing network (440, 441).
- Power Supply:** The circuit is powered by a dual-polarity supply (442, 443) connected to a common ground (444, 445). The positive rail (446, 447) and negative rail (448, 449) are distributed to the various stages.
- Handwritten Labels:** Numerous handwritten labels in the left margin identify specific components and sections, including 407, 412, 413, 414, 415, 416, 417, 418, 419, 420, 421, 422, 423, 424, 425, 426, 427, 428, 429, 430, 431, 432, 433, 434, 435, 436, 437, 438, 439, 440, 441, 442, 443, 444, 445, 446, 447, 448, 449, 450, 451, 452, 453, 454, 455, 456, 457, 458, 459, 460, 461, 462, 463, 464, 465, 466, 467, 468, 469, 470, 471, 472, 473, 474, 475, 476, 477, 478, 479, 480, 481, 482, 483, 484, 485, 486, 487, 488, 489, 490, 491, 492, 493, 494, 495, 496, 497, 498, 499, 500, 501, 502, 503, 504, 505, 506, 507, 508, 509, 510, 511, 512, 513, 514, 515, 516, 517, 518, 519, 520, 521, 522, 523, 524, 525, 526, 527, 528, 529, 530, 531, 532, 533, 534, 535, 536, 537, 538, 539, 540, 541, 542, 543, 544, 545, 546, 547, 548, 549, 550, 551, 552, 553, 554, 555, 556, 557, 558, 559, 560, 561, 562, 563, 564, 565, 566, 567, 568, 569, 570, 571, 572, 573, 574, 575, 576, 577, 578, 579, 580, 581, 582, 583, 584, 585, 586, 587, 588, 589, 590, 591, 592, 593, 594, 595, 596, 597, 598, 599, 600, 601, 602, 603, 604, 605, 606, 607, 608, 609, 610, 611, 612, 613, 614, 615, 616, 617, 618, 619, 620, 621, 622, 623, 624, 625, 626, 627, 628, 629, 630, 631, 632, 633, 634, 635, 636, 637, 638, 639, 640, 641, 642, 643, 644, 645, 646, 647, 648, 649, 650, 651, 652, 653, 654, 655, 656, 657, 658, 659, 660, 661, 662, 663, 664, 665, 666, 667, 668, 669, 670, 671, 672, 673, 674, 675, 676, 677, 678, 679, 680, 681, 682, 683, 684, 685, 686, 687, 688, 689, 690, 691, 692, 693, 694, 695, 696, 697, 698, 699, 700, 701, 702, 703, 704, 705, 706, 707, 708, 709, 710, 711, 712, 713, 714, 715, 716, 717, 718, 719, 720, 721, 722, 723, 724, 725, 726, 727, 728, 729, 730, 731, 732, 733, 734, 735, 736, 737, 738, 739, 740, 741, 742, 743, 744, 745, 746, 747, 748, 749, 750, 751, 752, 753, 754, 755, 756, 757, 758, 759, 760, 761, 762, 763, 764, 765, 766, 767, 768, 769, 770, 771, 772, 773, 774, 775, 776, 777, 778, 779, 780, 781, 782, 783, 784, 785, 786, 787, 788, 789, 790, 791, 792, 793, 794, 795, 796, 797, 798, 799, 800, 801, 802, 803, 804, 805, 806, 807, 808, 809, 810, 811, 812, 813, 814, 815, 816, 817, 818, 819, 820, 821, 822, 823, 824, 825, 826, 827, 828, 829, 830, 831, 832, 833, 834, 835, 836, 837, 838, 839, 840, 841, 842, 843, 844, 845, 846, 847, 848, 849, 850, 851, 852, 853, 854, 855, 856, 857, 858, 859, 860, 861, 862, 863, 864, 865, 866, 867, 868, 869, 870, 871, 872, 873, 874, 875, 876, 877, 878, 879, 880, 881, 882, 883, 884, 885, 886, 887, 888, 889, 890, 891, 892, 893, 894, 895, 896, 897, 898, 899, 900, 901, 902, 903, 904, 905, 906, 907, 908, 909, 910, 911, 912, 913, 914, 915, 916, 917, 918, 919, 920, 921, 922, 923, 924, 925, 926, 927, 928, 929, 930, 931, 932, 933, 934, 935, 936, 937, 938, 939, 940, 941, 942, 943, 944, 945, 946, 947, 948, 949, 950, 951, 952, 953, 954, 955, 956, 957, 958, 959, 960, 961, 962, 963, 964, 965, 966, 967, 968, 969, 970, 971, 972, 973, 974, 975, 976, 977, 978, 979, 980, 981, 982, 983, 984, 985, 986, 987, 988, 989, 990, 991, 992, 993, 994, 995, 996, 997, 998, 999, 1000.

FIG. 348

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440
24

20442

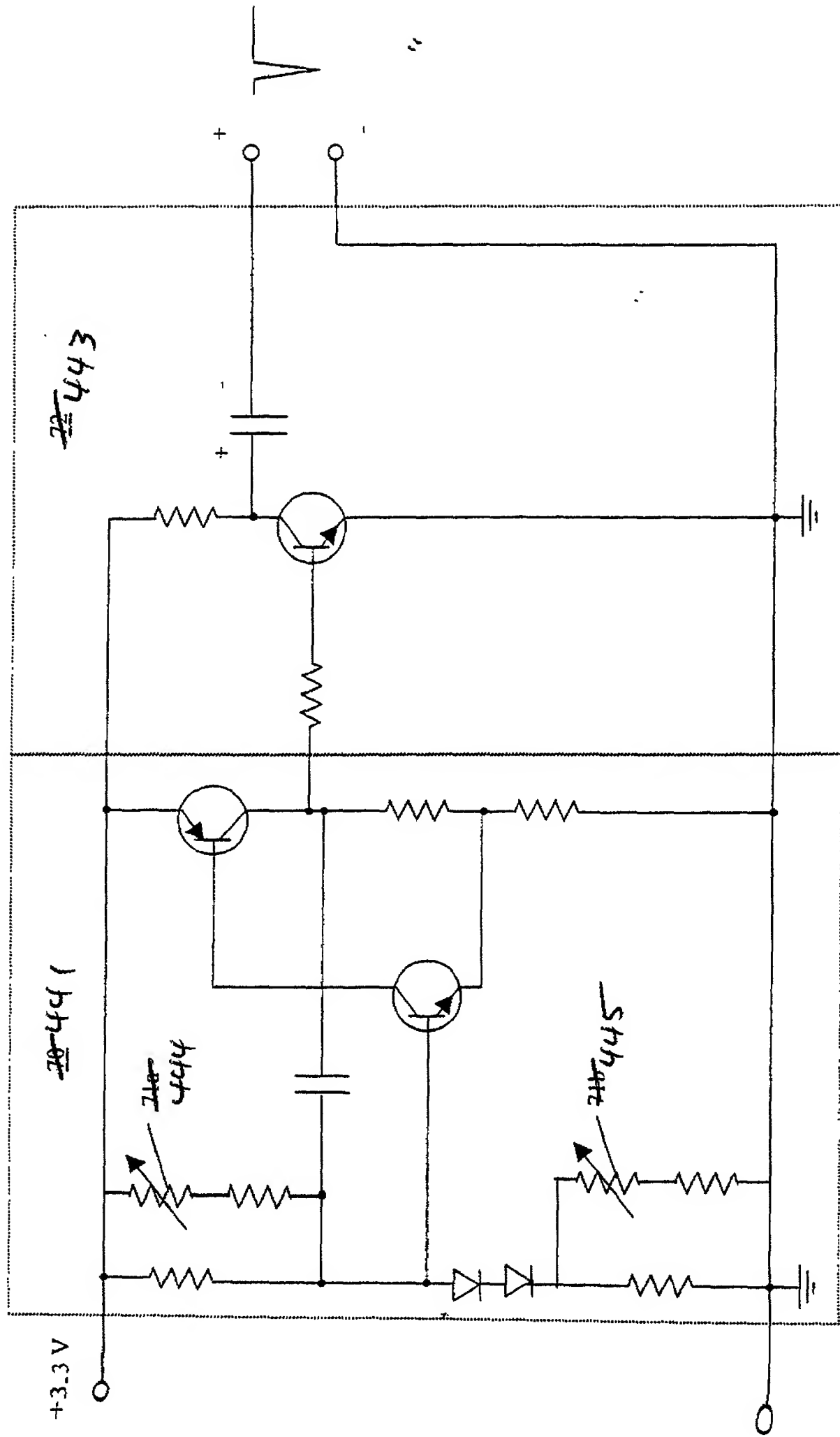


FIG. 449

20439

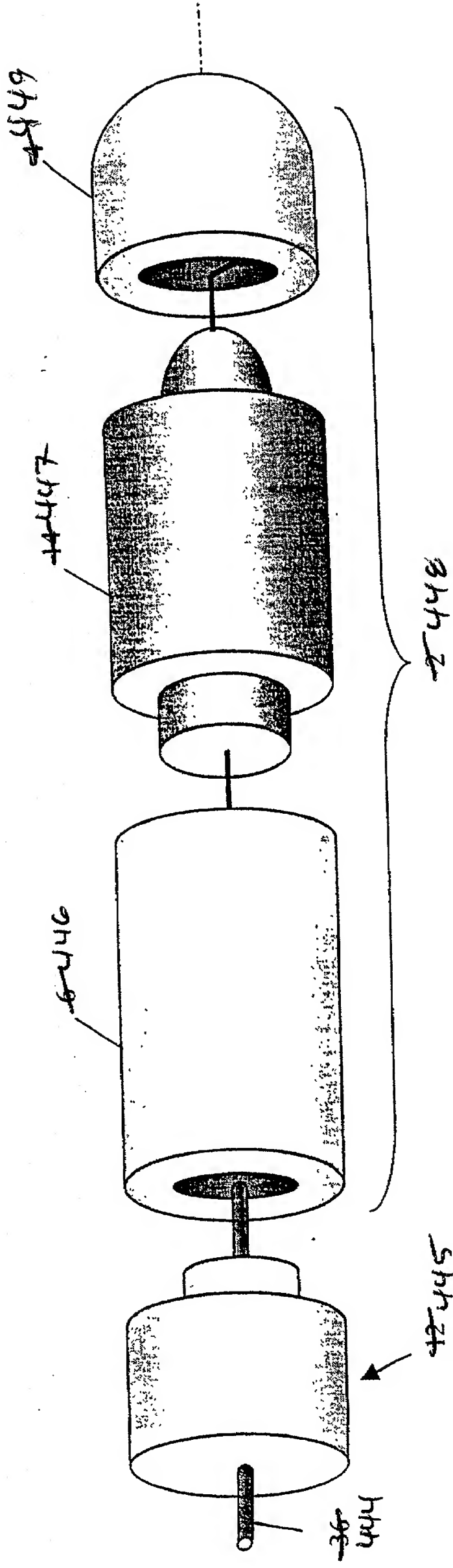


FIG. 250

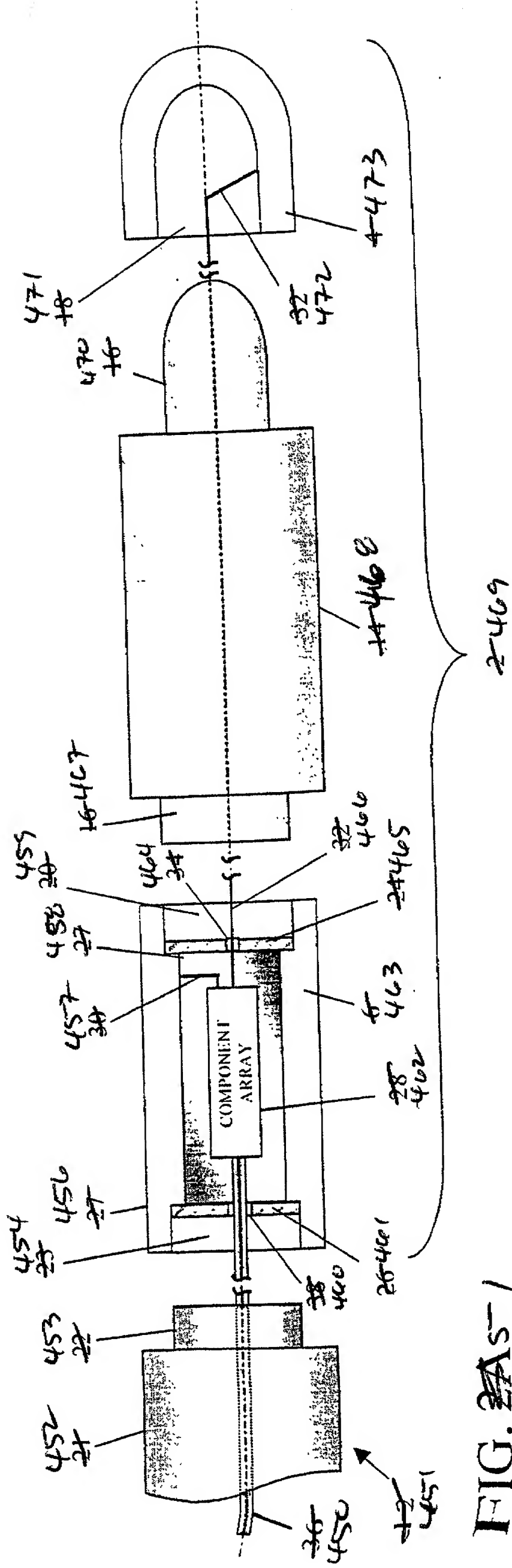
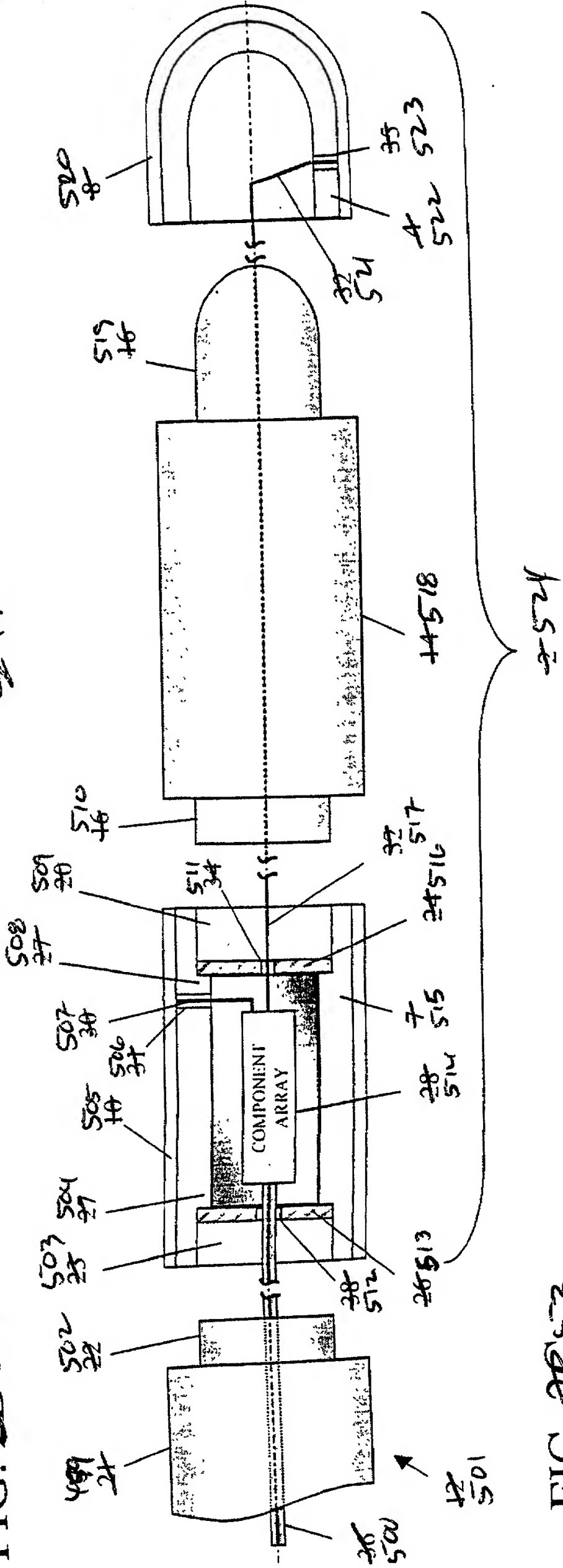
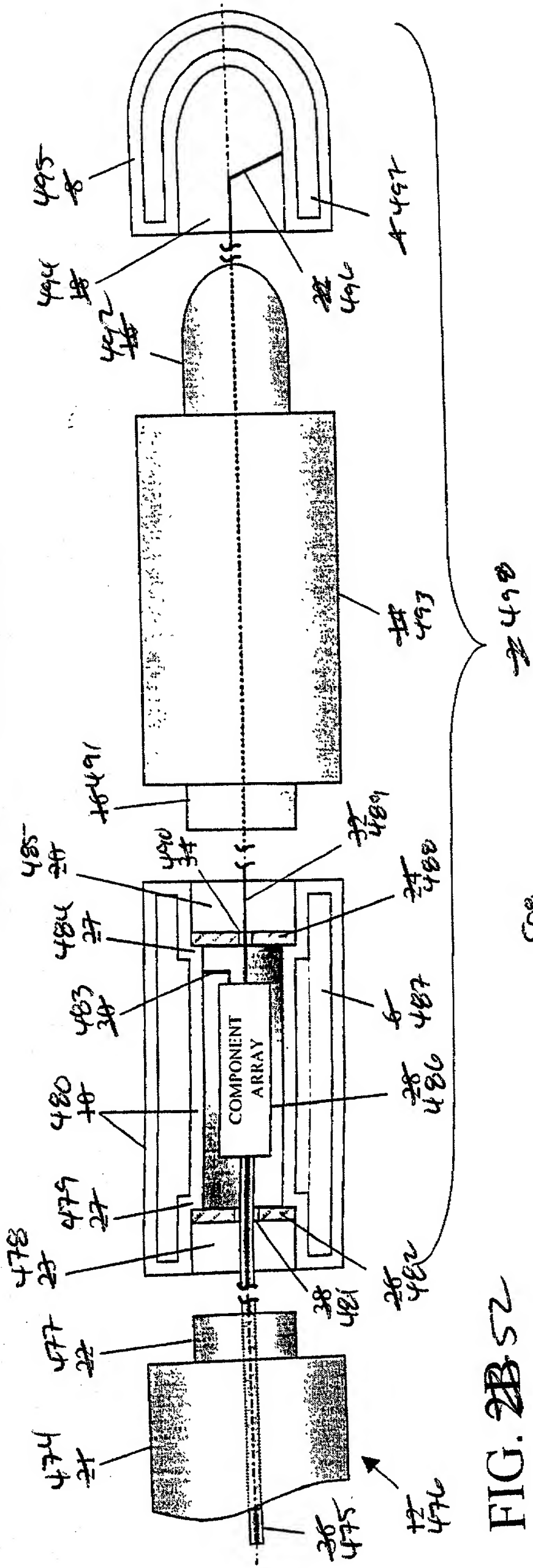


FIG. 251



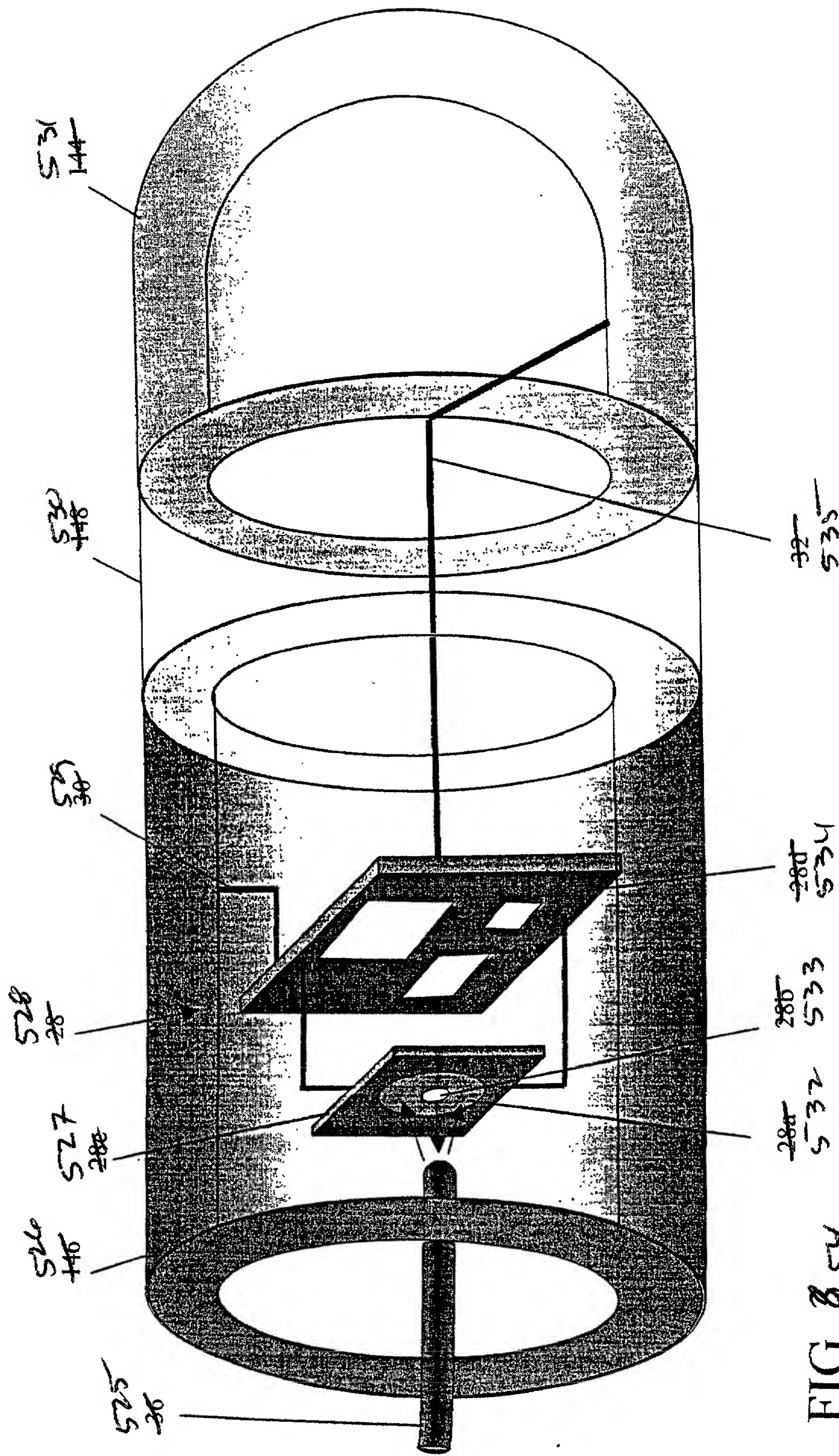


FIG. 8

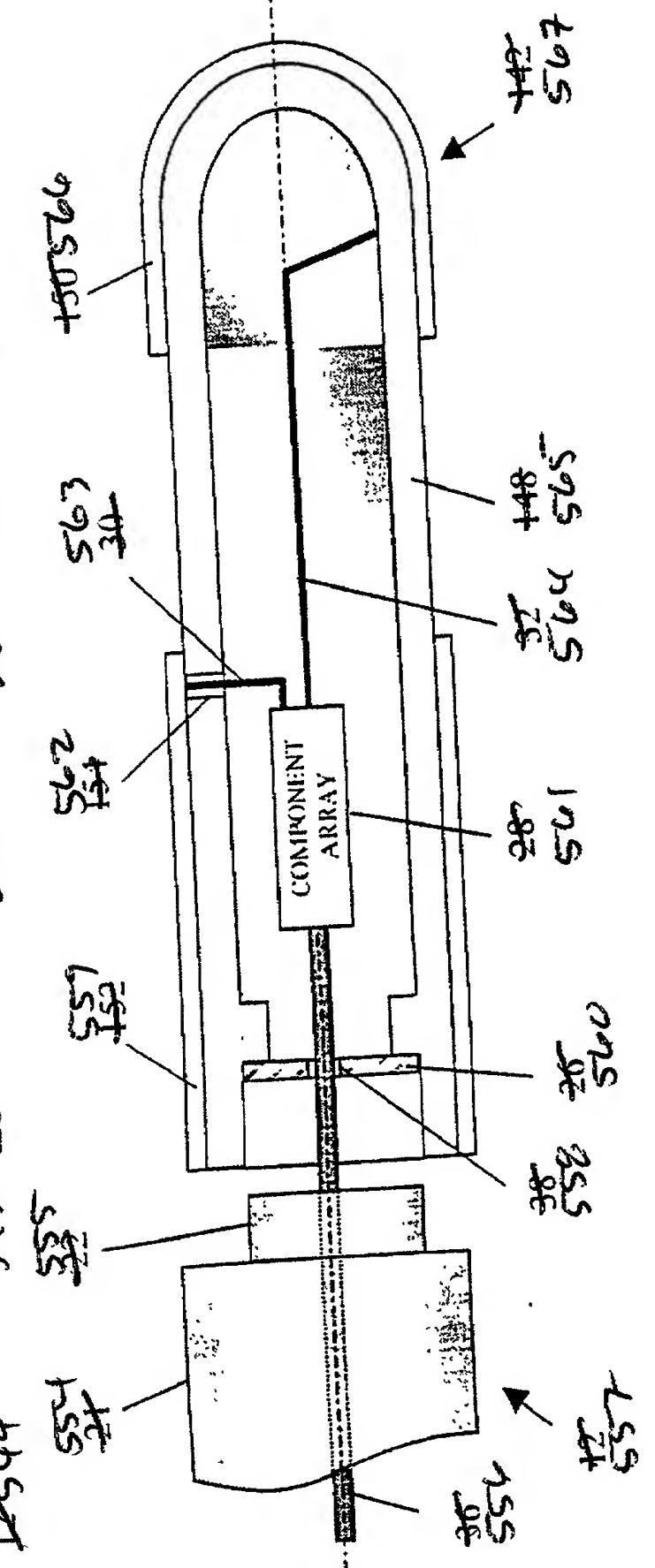
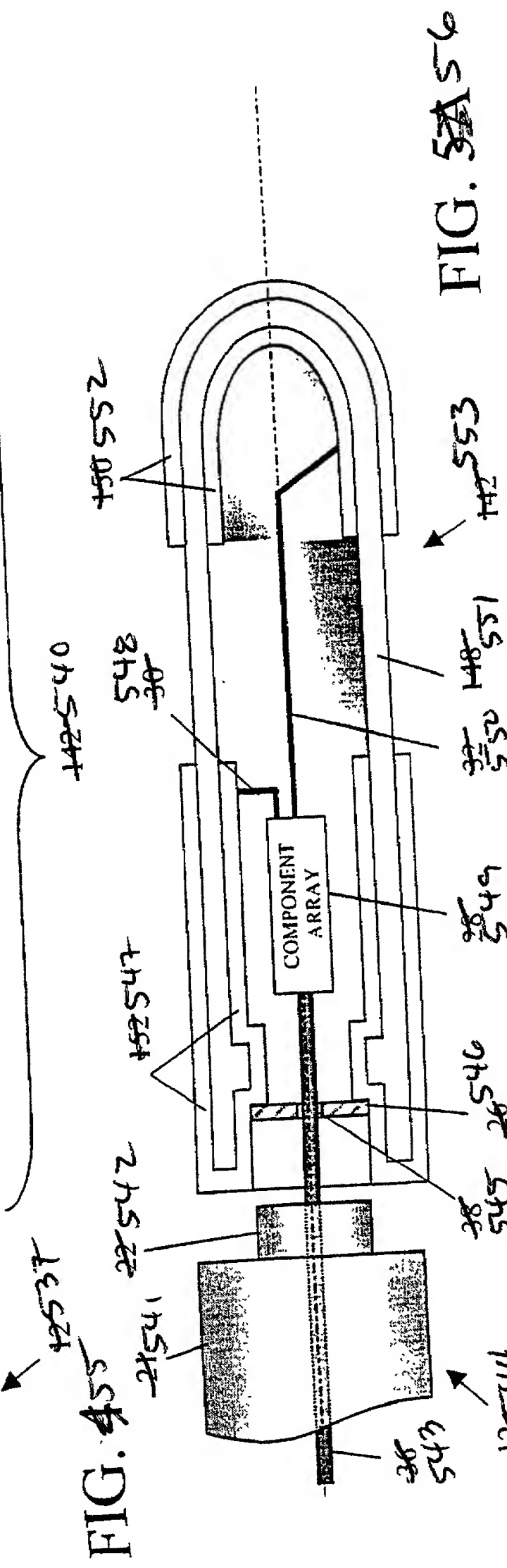
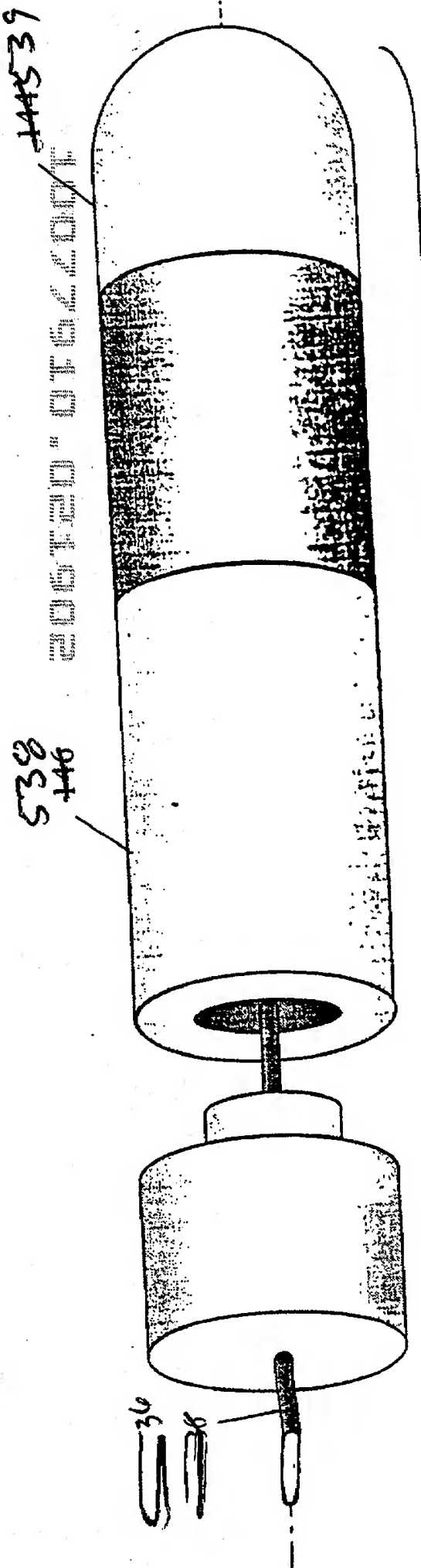


FIG. 5B57

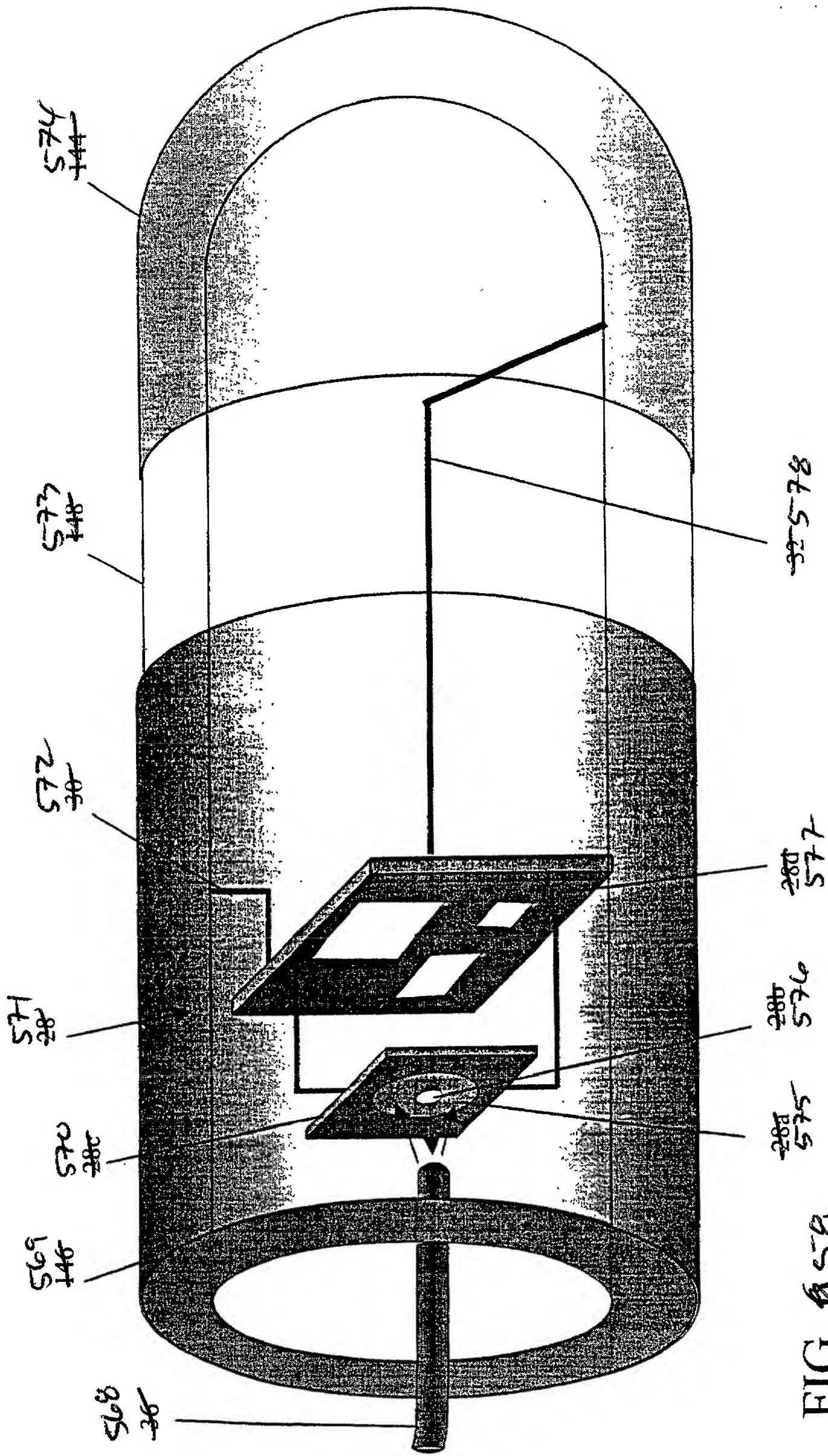


FIG. 58

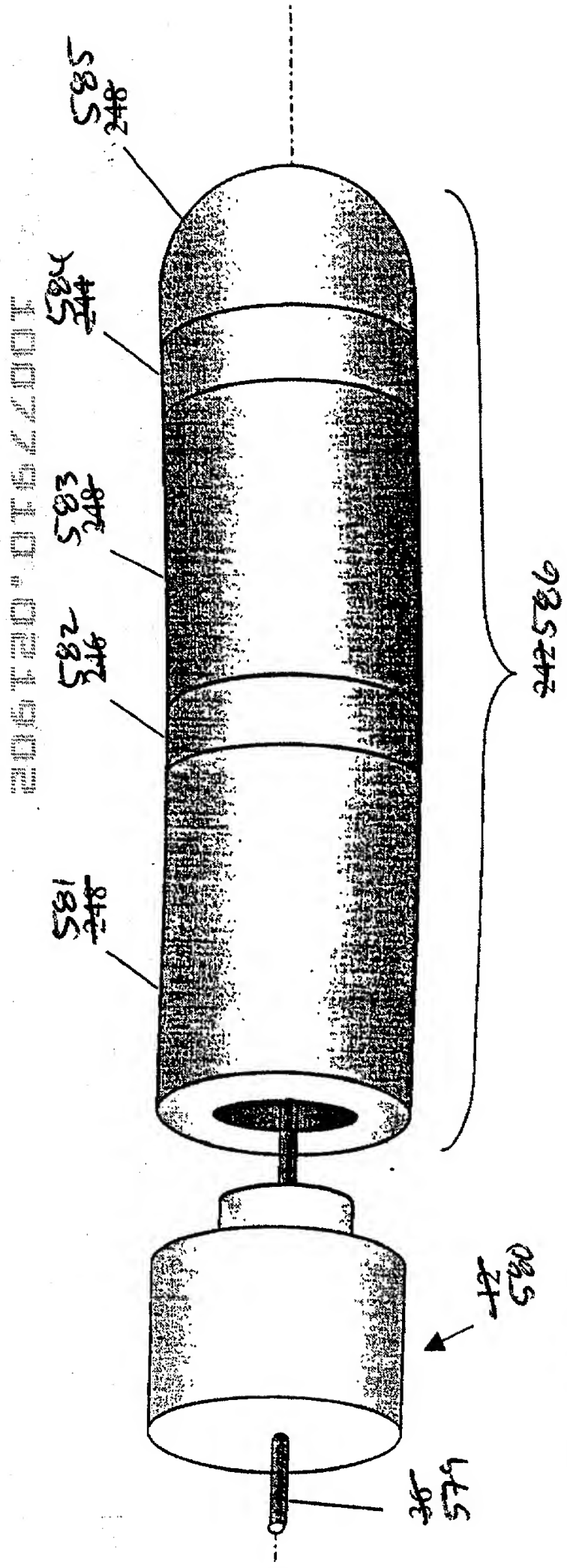
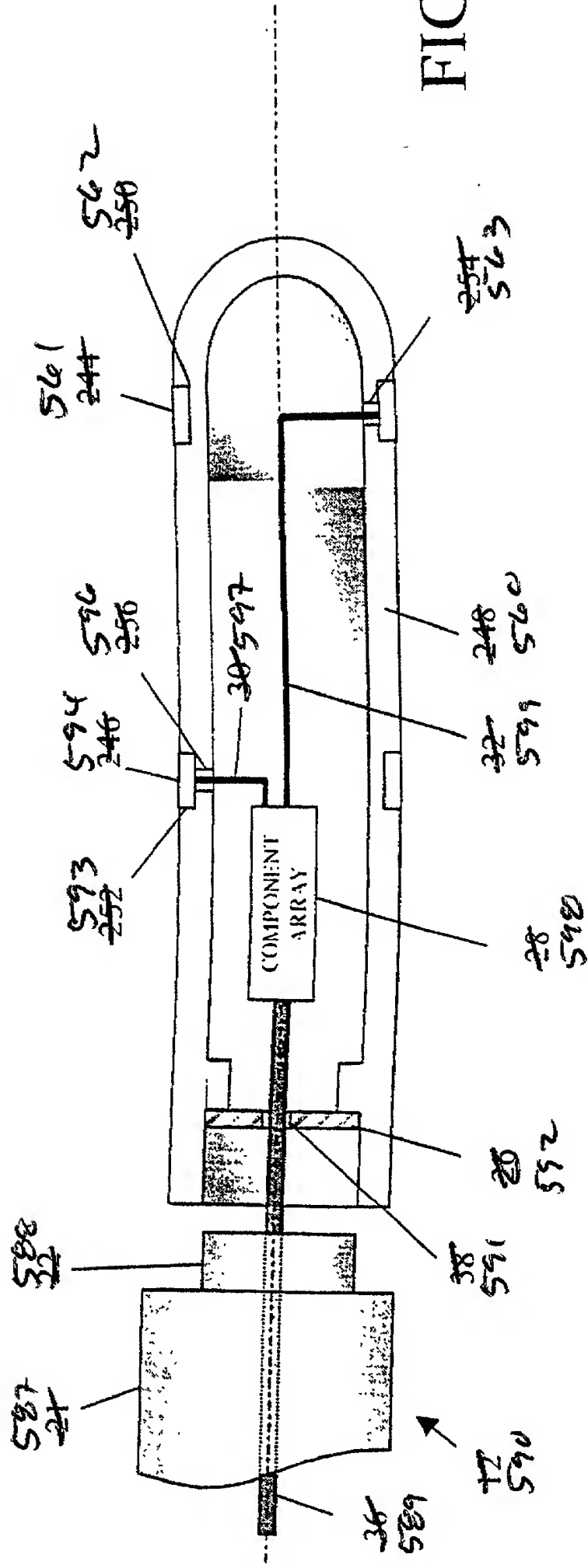


FIG. 59



1030

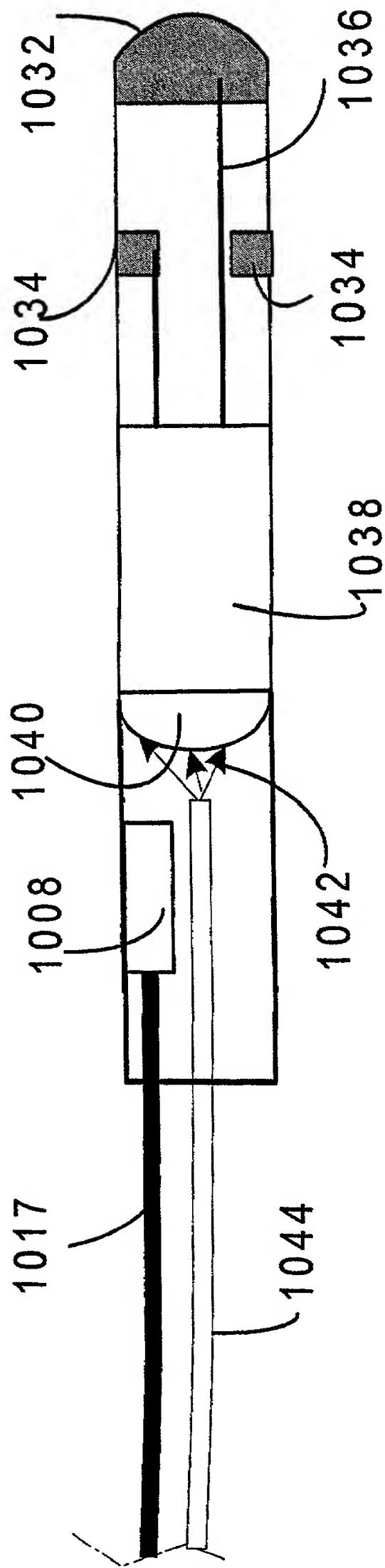


Fig. 10a62

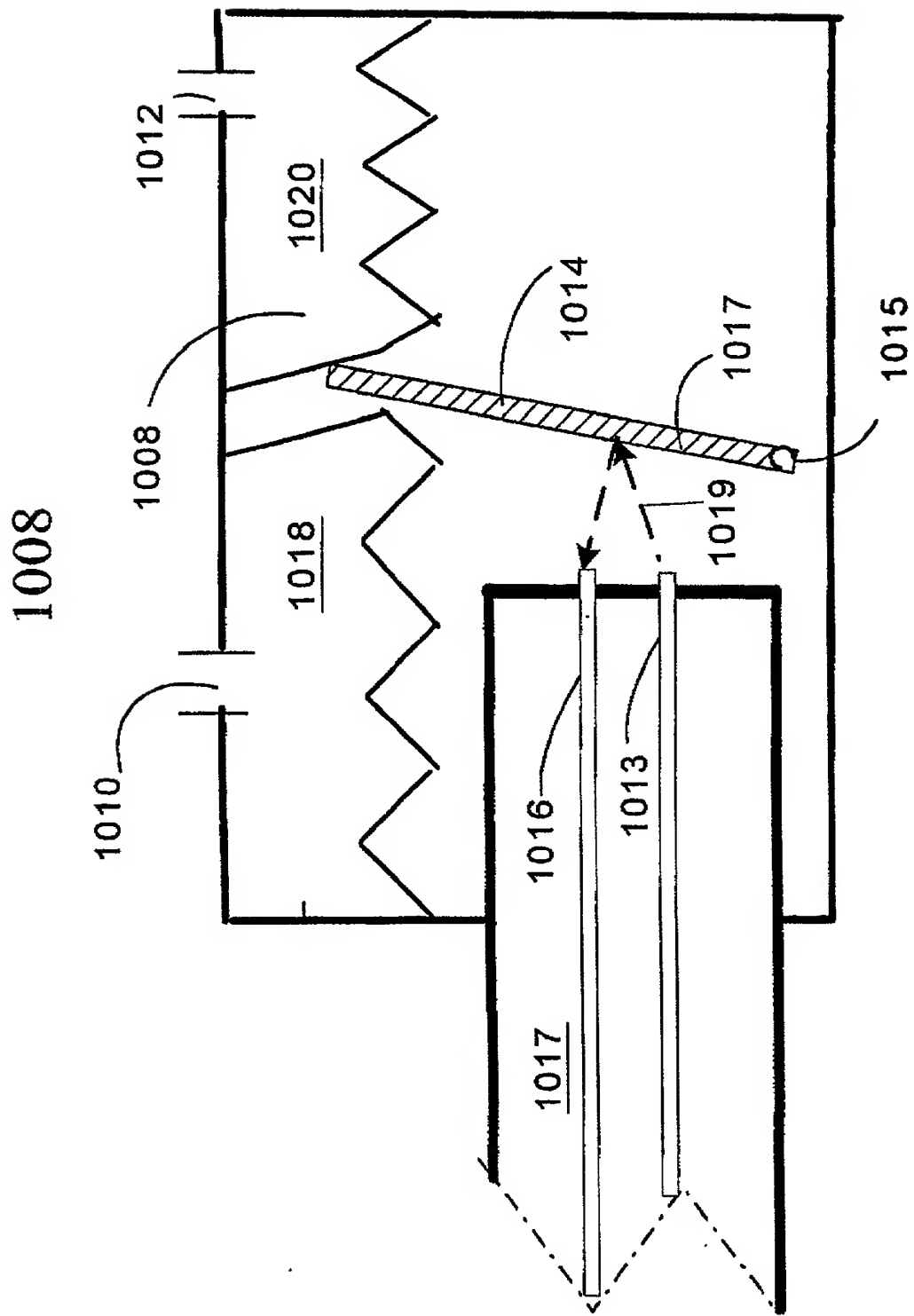
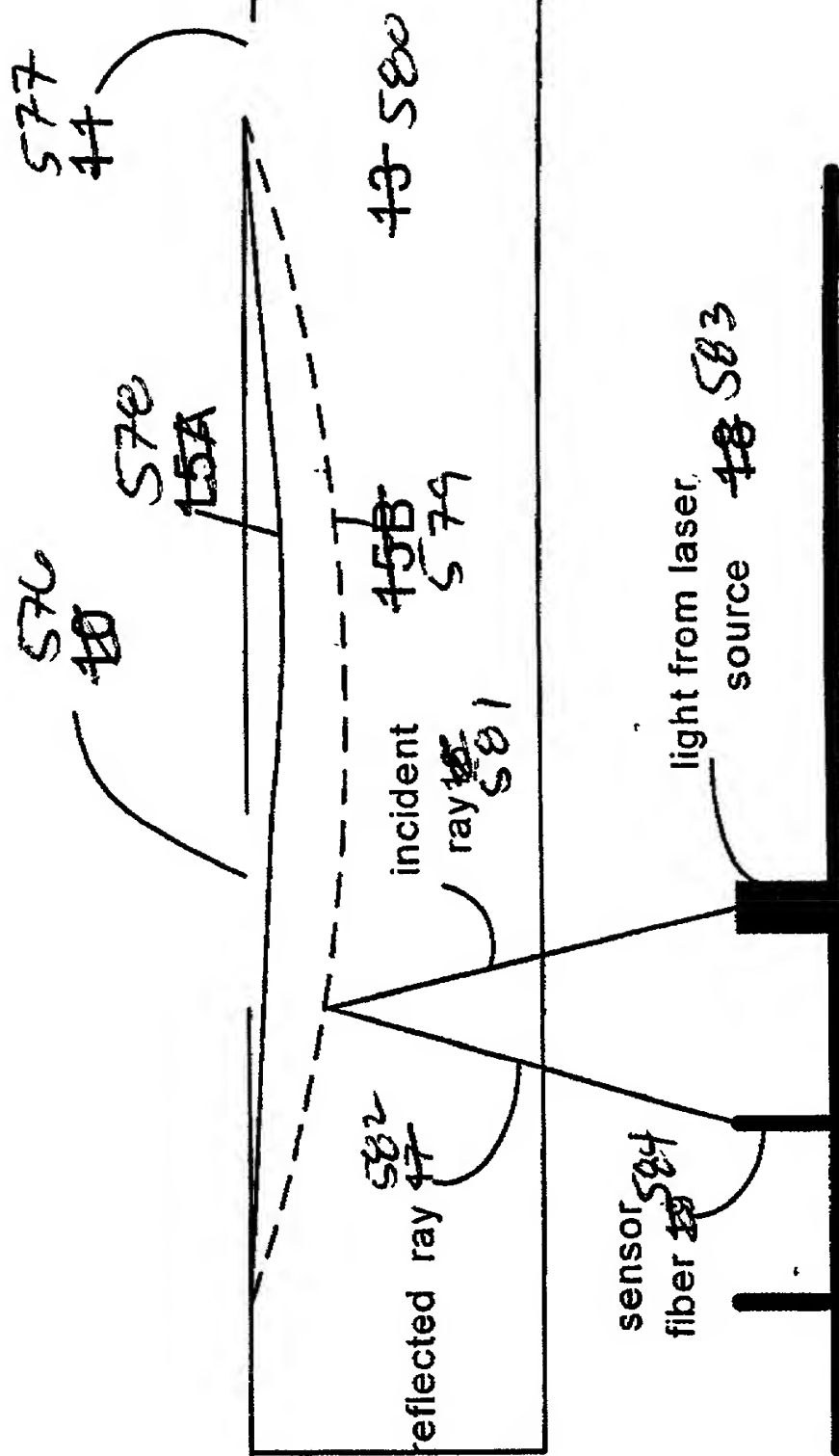


Fig. 10e₆₄

Figure 65



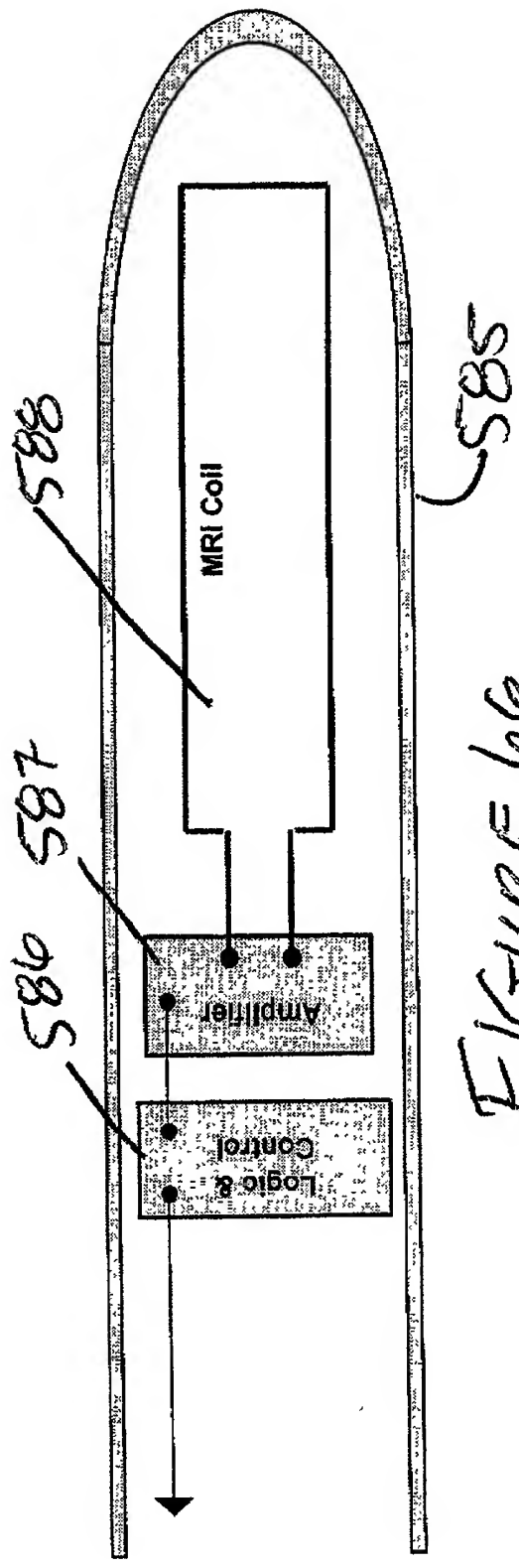


FIGURE 66

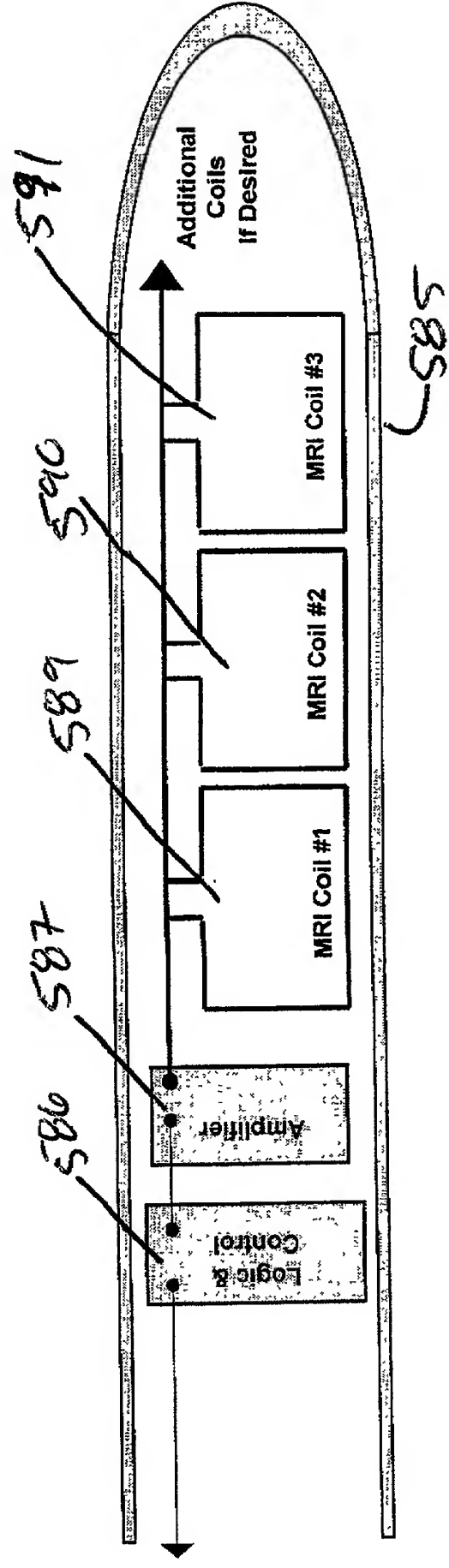


FIGURE 67

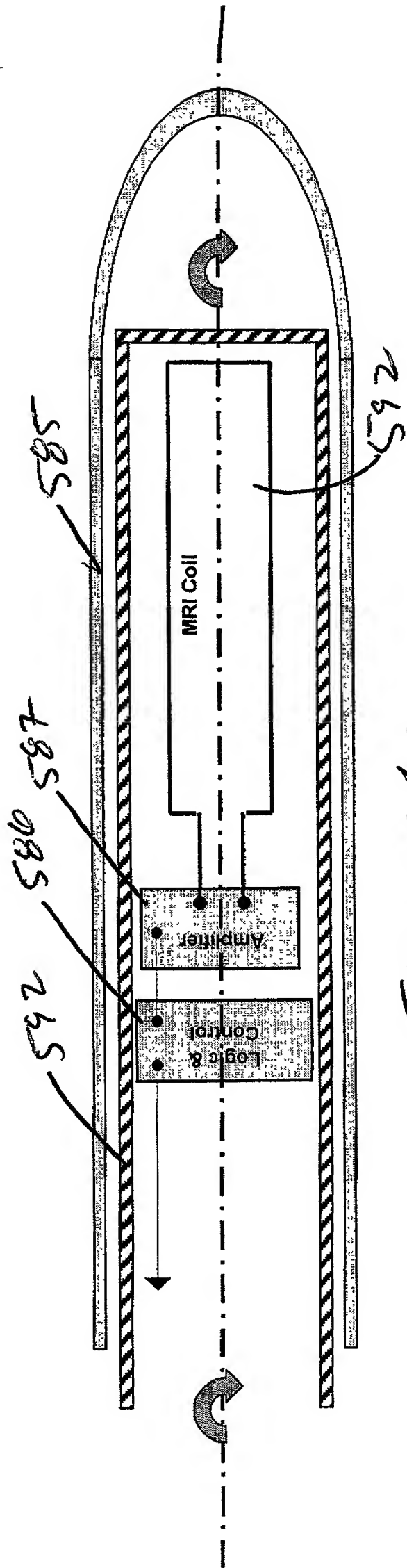


FIGURE 68

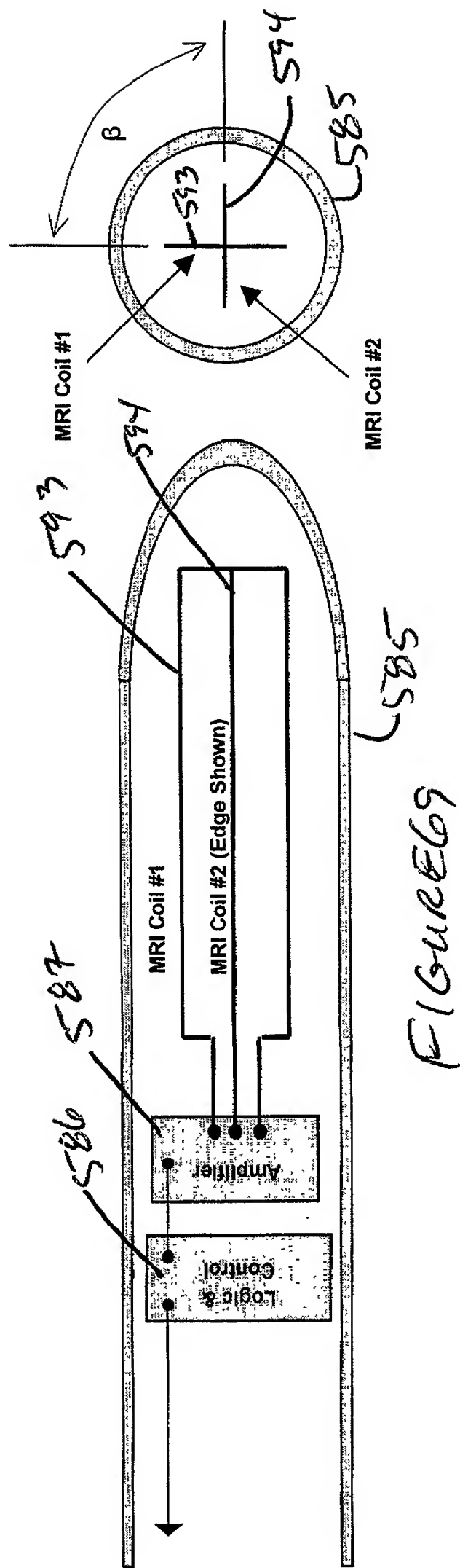


FIGURE 69

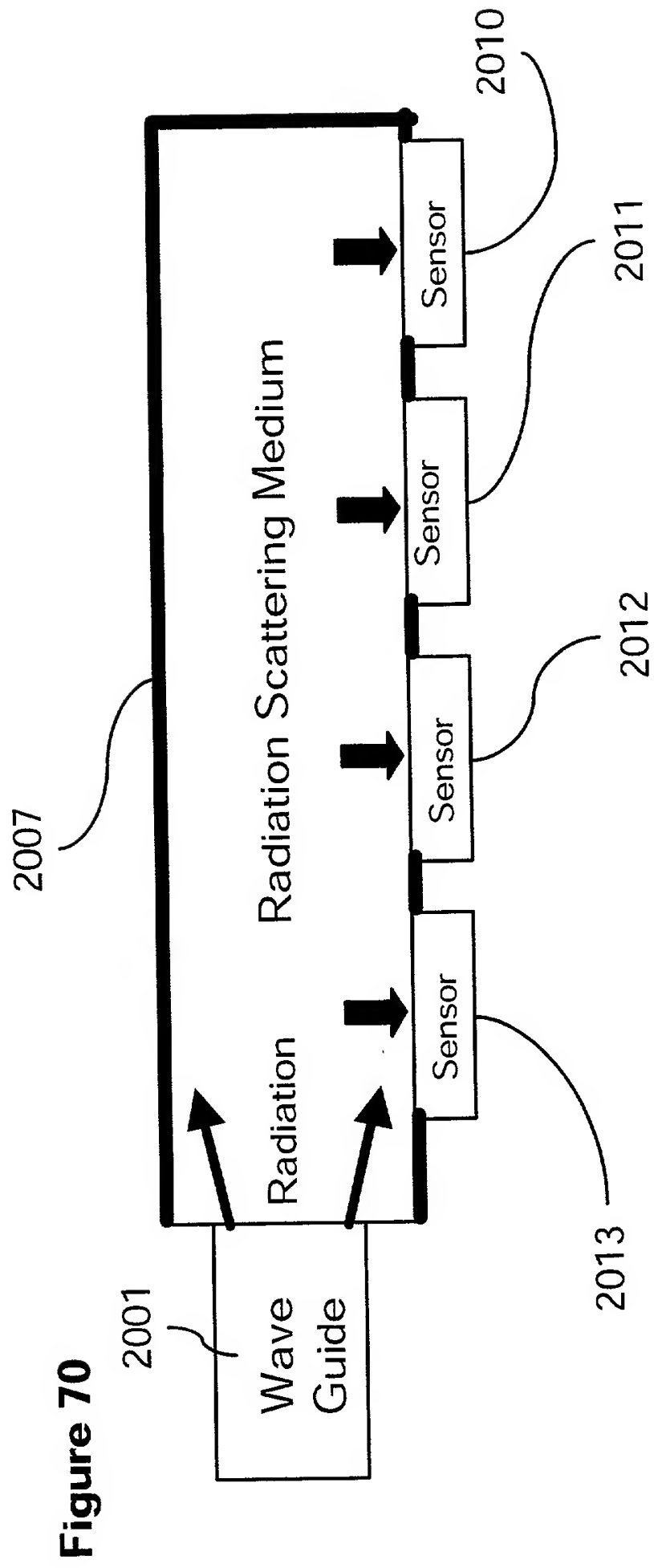


Figure 73

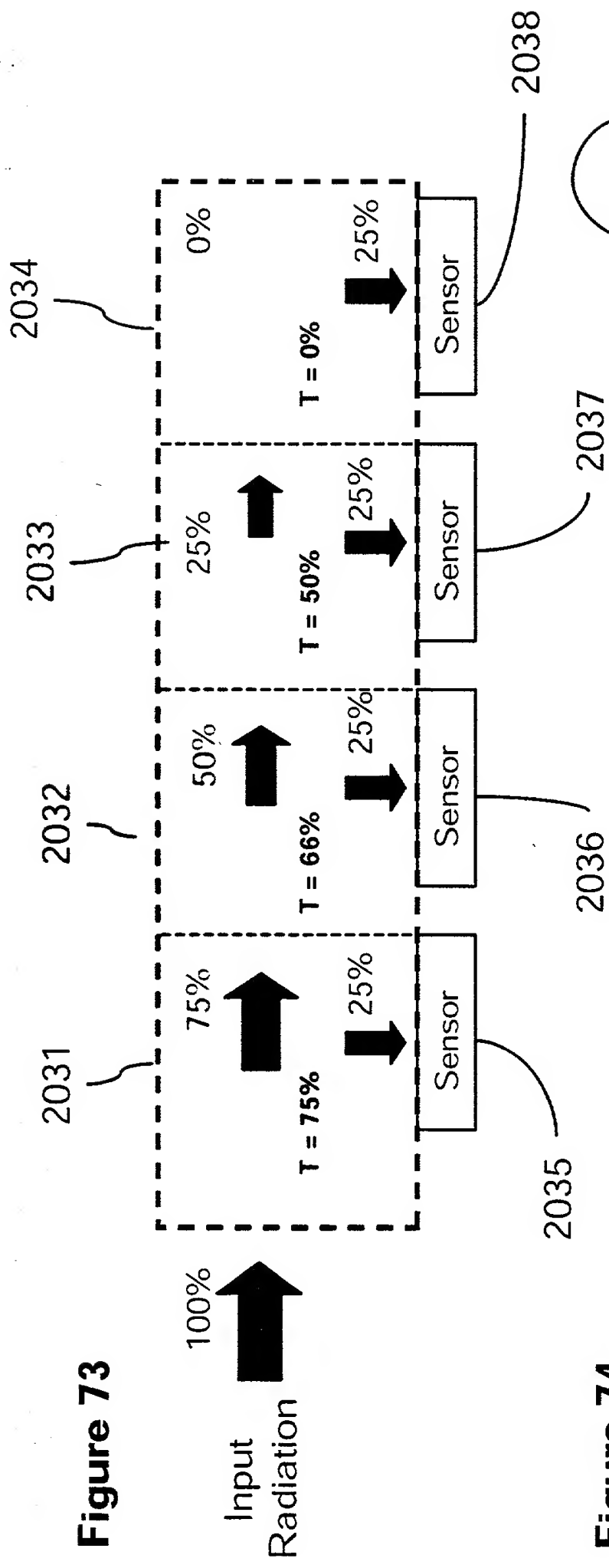
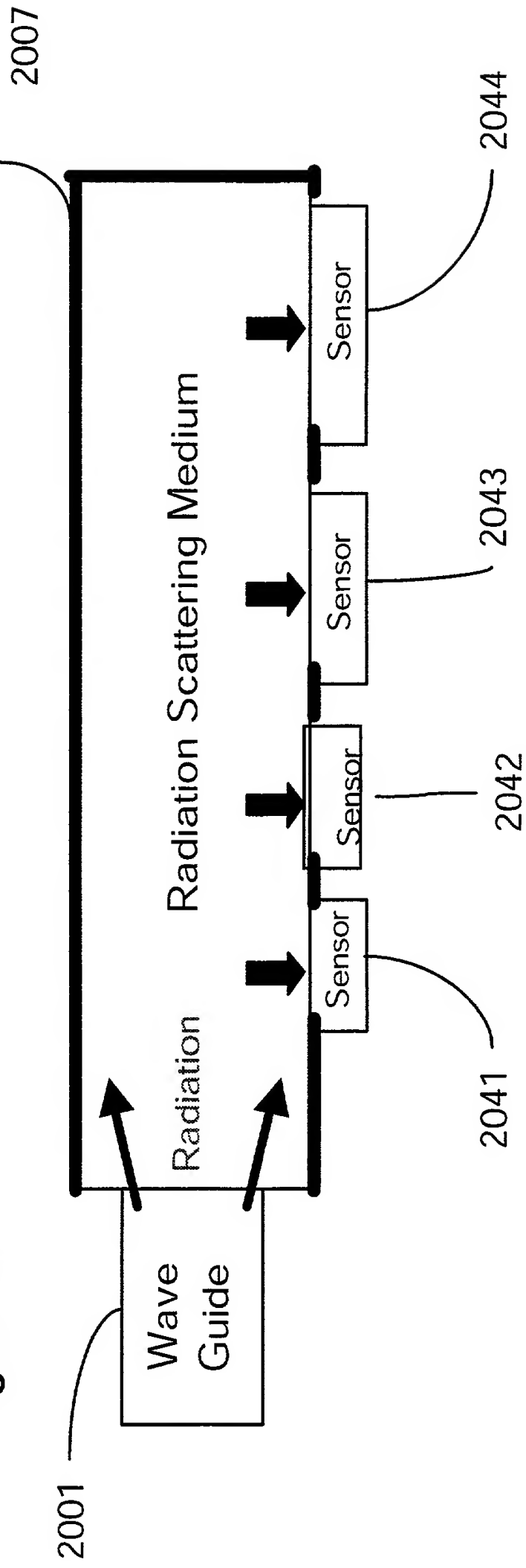


Figure 74



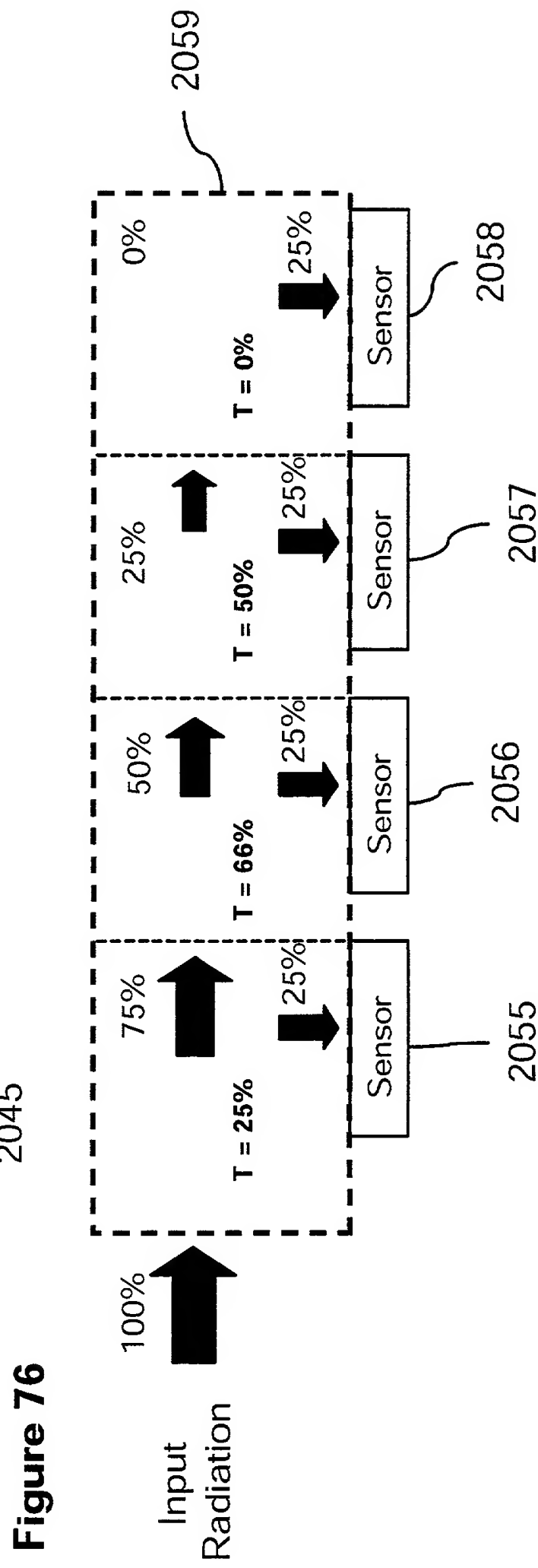
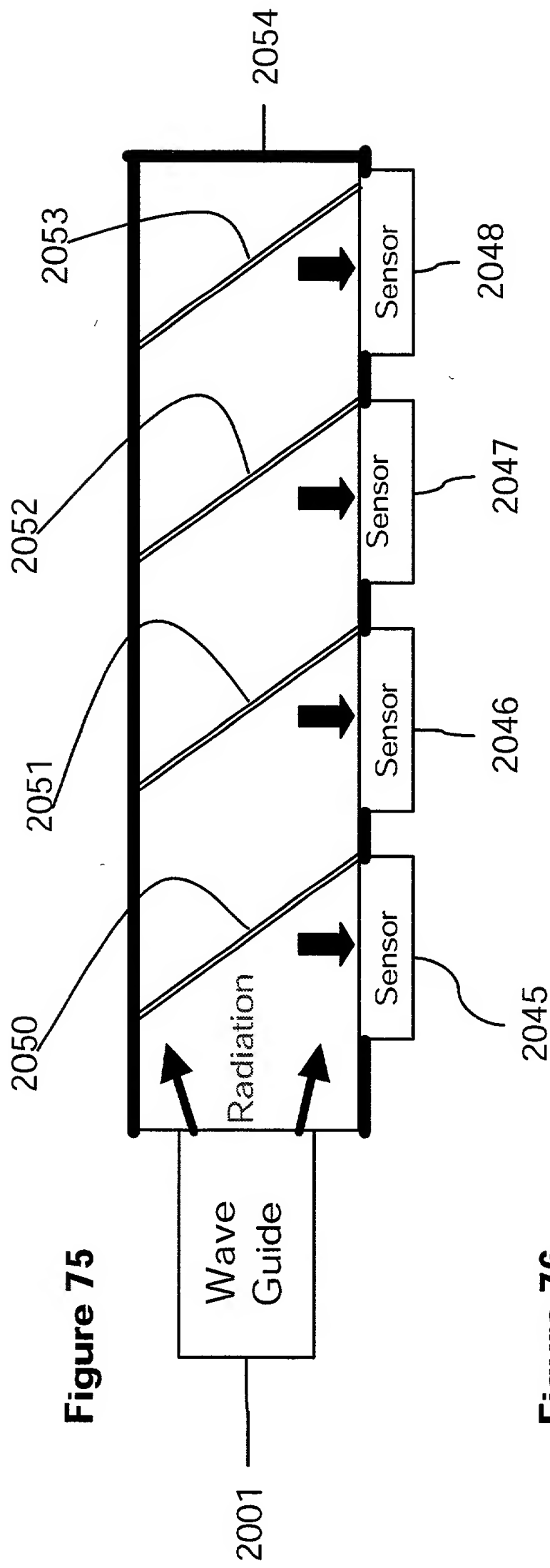


Figure 77

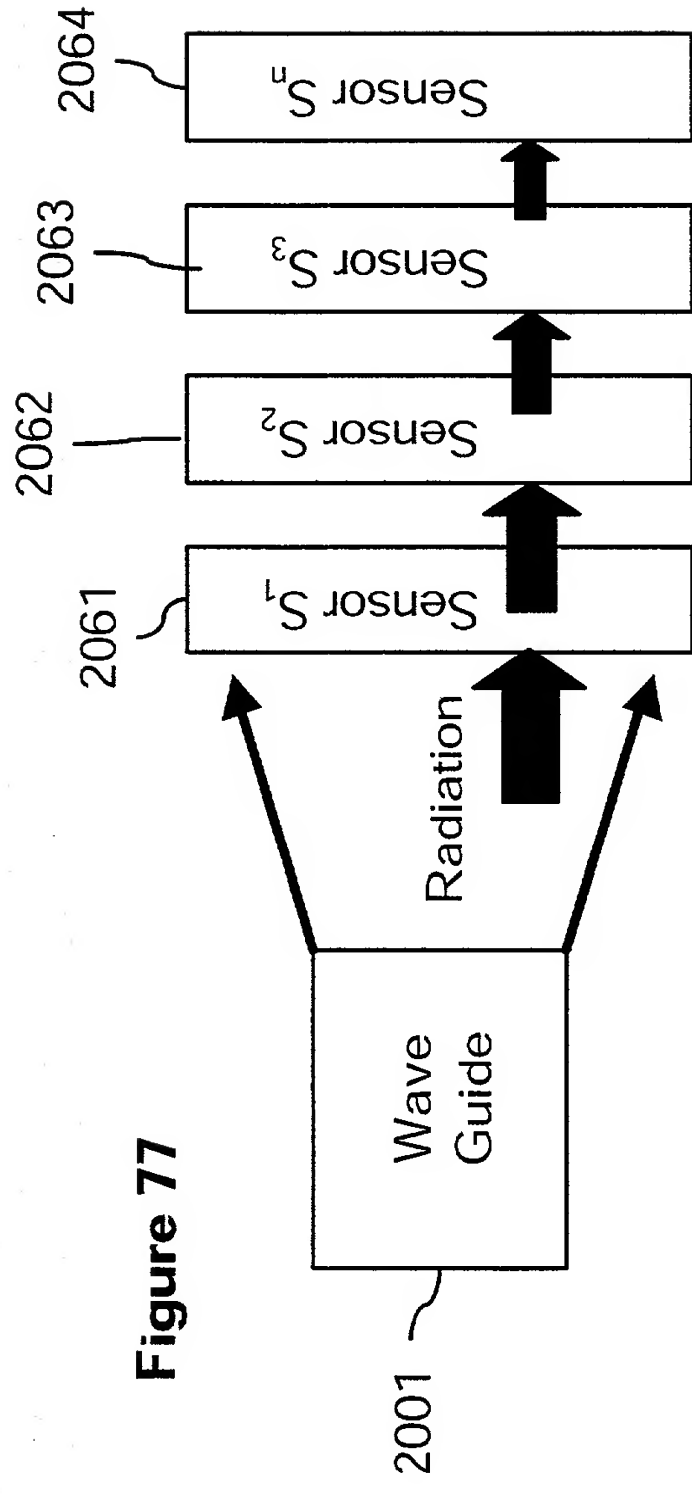
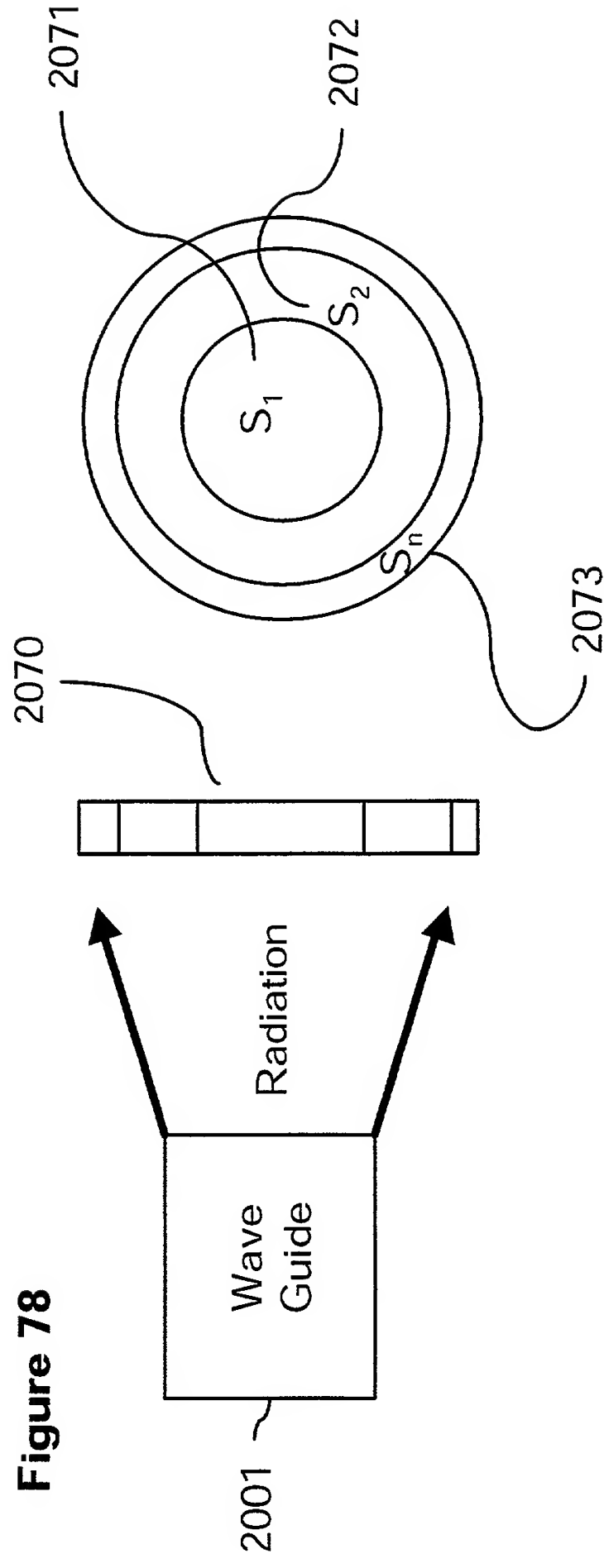


Figure 78



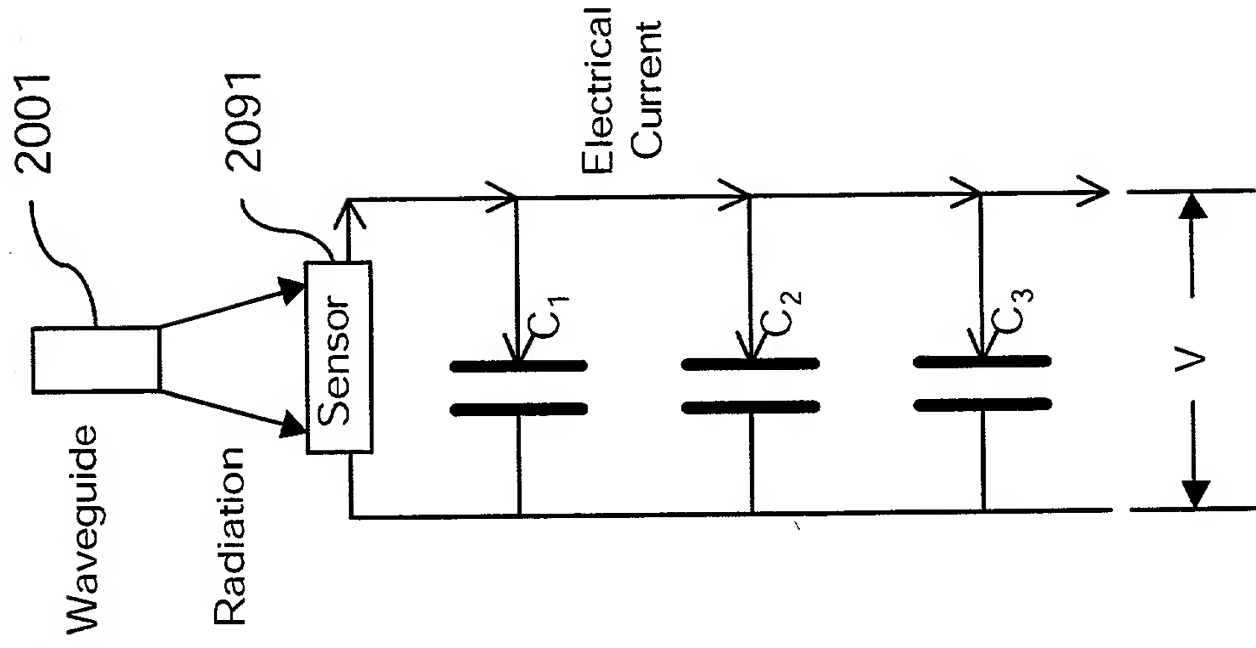


Figure 80

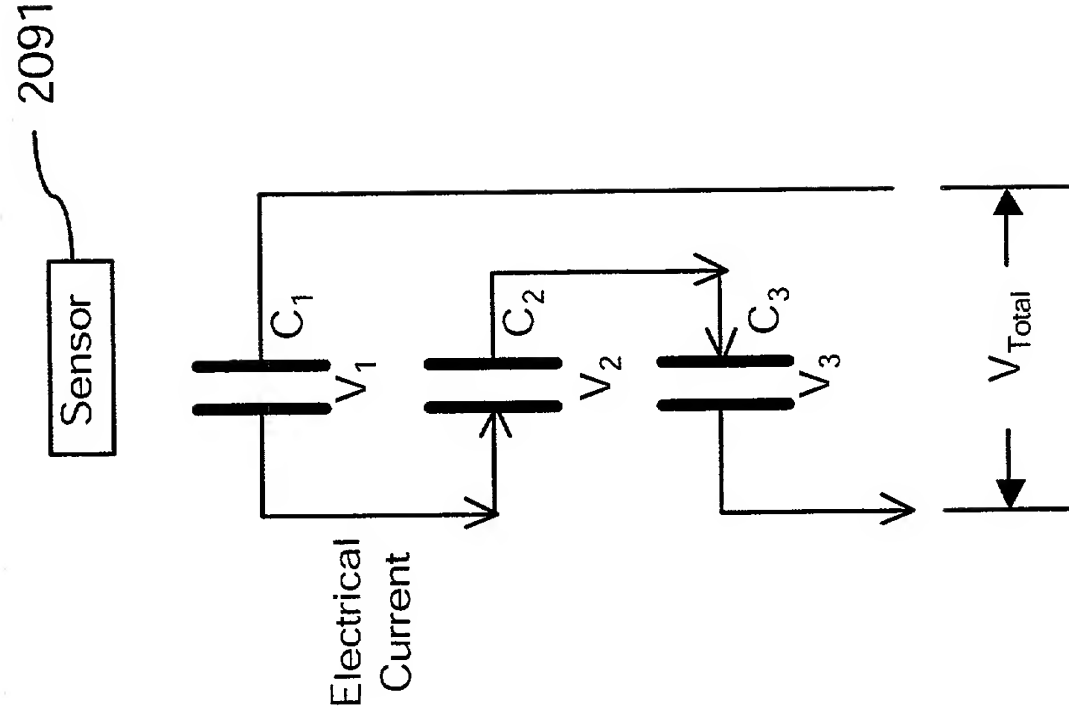


Figure 81

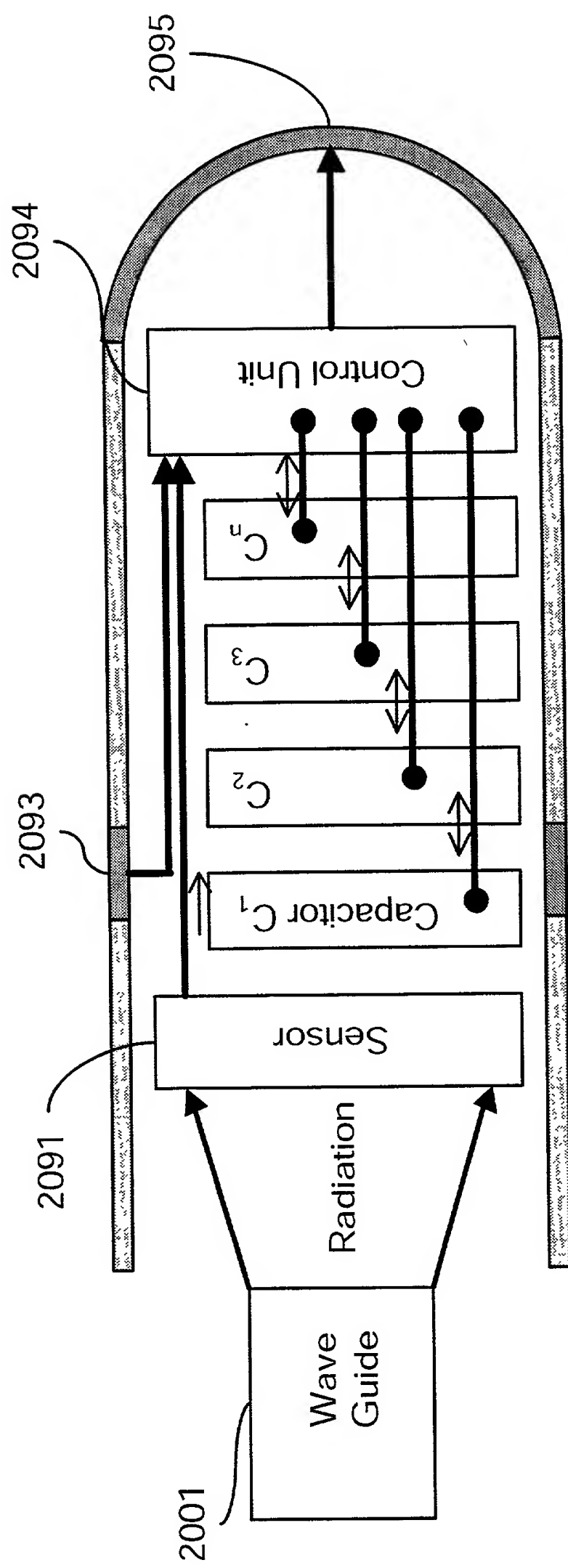


Figure 82

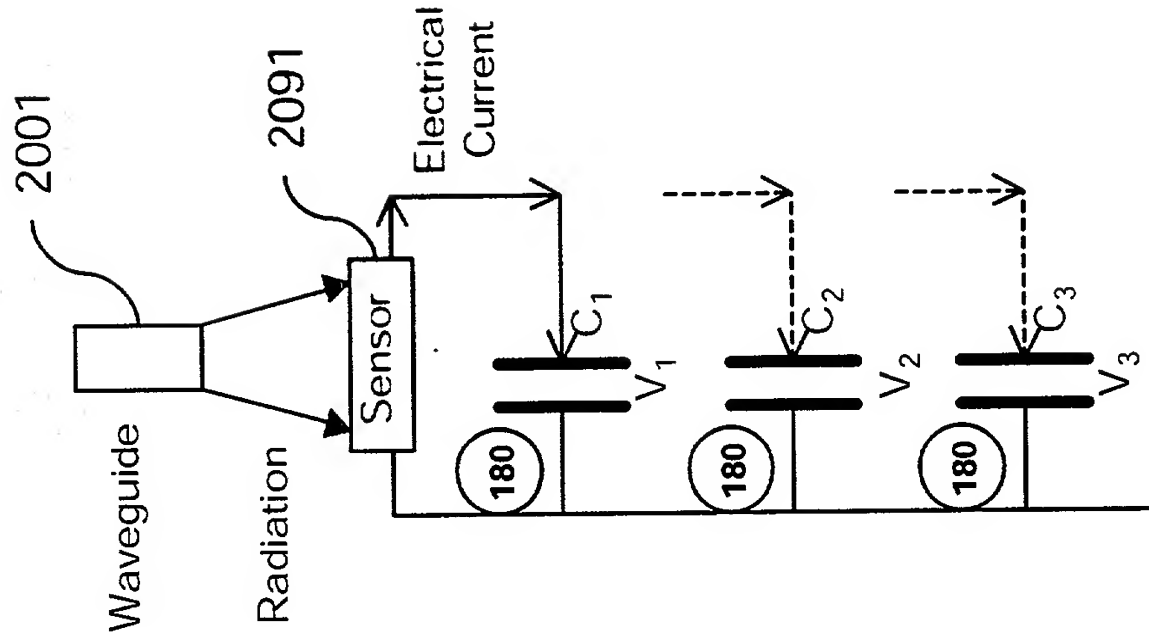


Figure 83

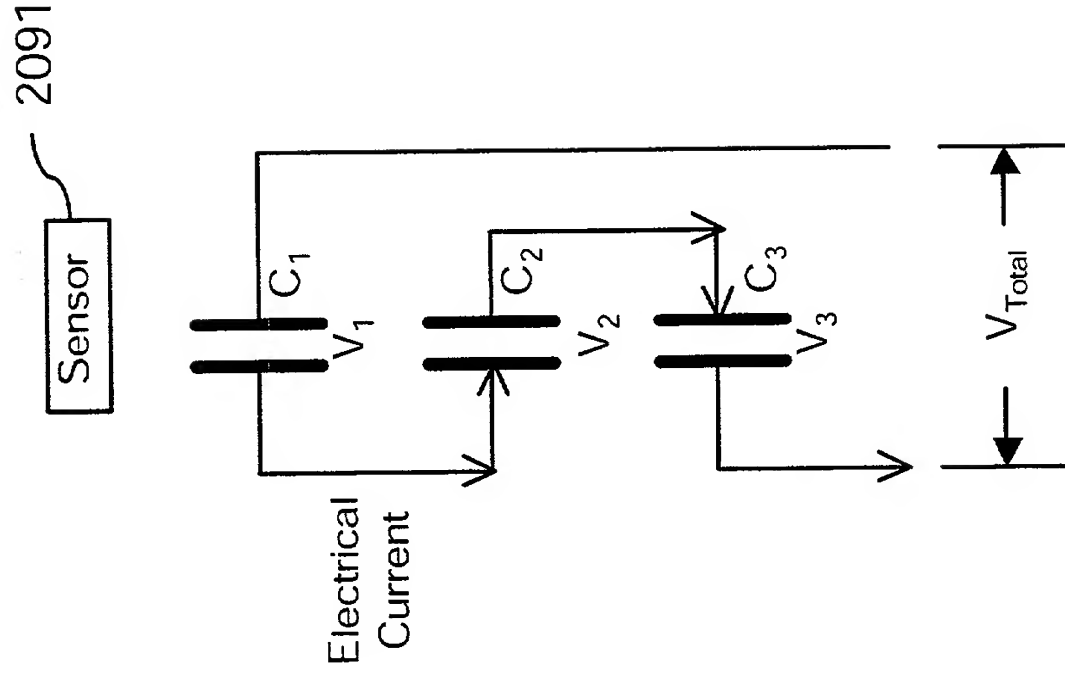


Figure 84

